



晶采光電科技股份有限公司
AMPIRE CO., LTD.

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SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-640480G2TNQW-TA0H
APPROVED BY	
DATE	

☐ Approved For Specifications

☒ **Approved For Specifications & Sample**

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2009/9/1	--	New Release	JOHN

1. INTRODUCTION

This is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device . This model is composed of a 5.7inch TFT-LCD panel , a driving circuit, touch panel and LED backlight system . This TFT-LCD has a high resolution (640(R.G.B) X 480) and can display up to 262,144 colors.

1-1. Features

- VGA Resolution
- 6 Bits color driver with LVDS interface
- Wide range operation temperature

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
Display resolution(dot)	640RGB (W) x 480(H)	dots
Display area	115.2 (W) x 86.4 (H)	mm
Pixel pitch	0.18 (W) x 0.18 (H)	mm
Color configuration	R.G.B Vertical stripe	
Overall dimension	127.0(W)x98.43(H)x9.1(D)---(Typ)	mm
Surface treatment	Antiglare , Hard-Coating(3H)	
Brightness	400	cd/m ²
Contrast ratio	250 : 1	
Backlight unit	LED	
Display color	262,144	colors
Viewing Direction	12 o'clock	
Display Mode	Normally White	

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN	MAX	UNIT	NOTE
Power Supply Voltage	V _{cc}	-0.5	5	V	
Signal Input Voltage	DCLK, DE R0~R5 G0~G5 B0~B5	-0.5	V _{cc} + 0.5	V	
Operation Temperature	Top	-20	70	°C	
Storage Temperature	Tstg	-30	80	°C	

4. ELECTRICAL CHARACTERISTICS

4-1 TFT LCD Module voltage

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Power Voltage For LCD	V _{CC}	3.0	3.3	3.6	V	
Power Voltage For VLED	V _{DD}	--	5.0	--	V	
Logic Input Voltage	V _{IH}	V _{CC} *0.7	--	V _{CC}	V	
	V _{IL}	0	--	V _{CC} *0.3	V	
ADJ Input Voltage	V _{IH}	3.0	--	5.0	V	
	V _{IL}	GND	--	0.3	V	

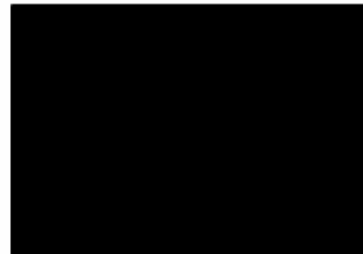
4-2 TFT LCD current consumption

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LCD Power Current	I _{cc}	-	106	-	mA	(1)
LED Power Current	I _{LED} (V _{LED} =5V)	-	290	-	mA	

NOTE : (1) Typ : under 64 gray pattern Max : under black pattern



(a) 64 Gray Pattern



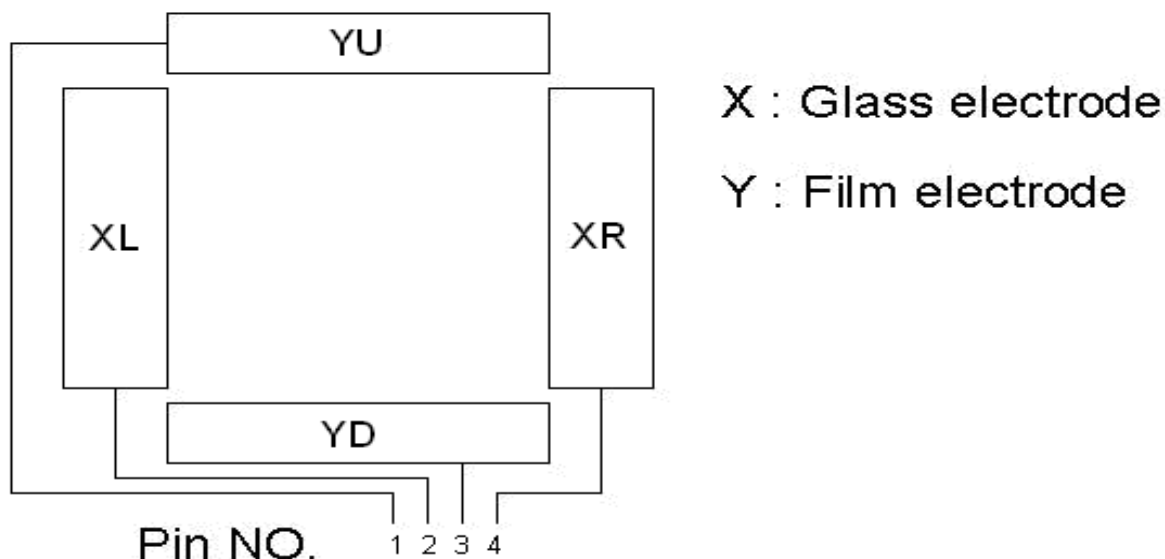
(b) Black Pattern

5. TOUCH PANEL ELECTRICAL SPECIFICATION

5.1 Touch Screen Panel Characteristics

1. Operation Temperature : $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$
Storage Temperature : $-30^{\circ}\text{C} \sim +80^{\circ}\text{C}$
2. Life Time : > 1,000,000 times
3. Linearity : $\leq 1.5\%$ after environmental & life test $\leq 3.0\%$
4. TOP ITO Film : Anti-Glare Hard Coating & Anti-Newton Ring
Sheet Resistance : $300\Omega \sim 1000\Omega$;
BOTTOM GLASS : Sheet Resistance : $100\Omega \sim 800\Omega$
5. Tai Type : FPC Gold-plated
6. Meet for ROHS.
7. Insulating Resistance : More than $20\text{M}\Omega$ at DC 25 V

5.2 Touch Screen Pane & Interface



Pin No.	Symbol	I/O	Function
1	YU	Top	Top electrode – differential analog
2	XL	Left	Left electrode – differential analog
3	YD	Bottom	Bottom electrode – differential analog
4	XR	Right	Right electrode – differential analog

6. INTERFACE

LVDS J2:

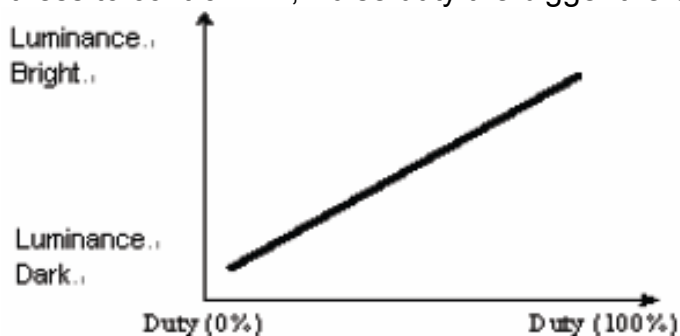
Pin no	Symbol	Function
1	VDD	POWER SUPPLY:3.3V
2	VDD	POWER SUPPLY:3.3V
3	Gnd	Power Ground
4	Gnd	Power Ground
5	IN0-	Transmission Data of Pixels
6	IN0+	Transmission Data of Pixels
7	Gnd	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	Gnd	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	Gnd	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	Gnd	Power Ground
17	NC	No Connect
18	NC	No Connect
19	Gnd	Power Ground
20	Gnd	Power Ground

LED J3:

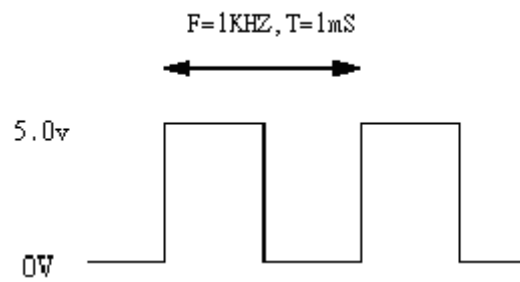
Pin no	Symbol	Function
1	GND	Power Ground
2	VLED	Power Supply for LED 5V
3	VLED	Power Supply for LED 5V
4	ADJ	Adjust for LED Brightness <i>*Note</i>

NOTE :

1. ADJ adjust brightness to control Pin , Pulse duty the bigger the brighter.



2. ADJ signal = 0 ~ 5.0V , operation frequency : 300Hz~1KHz



3. VSS Pin must ground contact , can not be floating.

7. AC Timing characteristic of the LVDS

Switching Characteristics

over recommended operating conditions (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
t_{su}	Setup time, D0–D20 to CLKOUT↓	5			ns
t_h	Data hold time, CLKOUT↓ to D0–D20	5			ns
$t_{(RSKM)}$	Receiver input skew margin ⁽²⁾ (see Figure 7)				
	$t_c = 15.38 \text{ ns } (\pm 0.2\%)$, Input clock jitter < 50 ps, ⁽³⁾	550	700		ps
t_d	Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7)				
	$V_{CC} = 3.3 \text{ V}$, $t_c = 15.38 \text{ ns } (\pm 0.2\%)$, $T_A = 25^\circ\text{C}$	3	5	7	ns
t_{en}	Enable time, $\overline{\text{SHTDN}}$ to phase lock		1		ms
t_{dis}	Disable time, $\overline{\text{SHTDN}}$ to off state		400		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (data only)		3		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (clock only)		1.5		ns
t_w	Pulse duration, output clock		0.50 t_c		ns

(1) All typical values are at $V_{CC} = 3.3 \text{ V}$, $T_A = 25^\circ\text{C}$.

(2) The parameter $t_{(RSKM)}$ is the timing margin available to allocate to the transmitter and interconnection skews and clock jitter. The value of this parameter at clock periods other than 15.38 ns can be calculated from $t_{RSKM} = t_c/14 - 550 \text{ ps}$.

(3) |Input clock jitter| is the magnitude of the change in input clock period.

PARAMETER MEASUREMENT INFORMATION (continued)

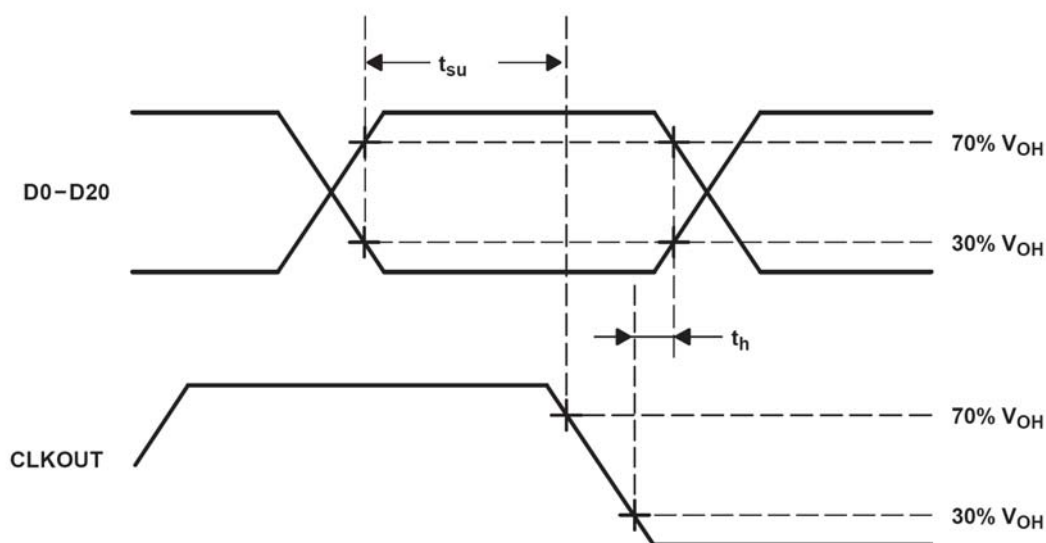


Figure 5. Setup and Hold Time Waveforms

PARAMETER MEASUREMENT INFORMATION (continued)

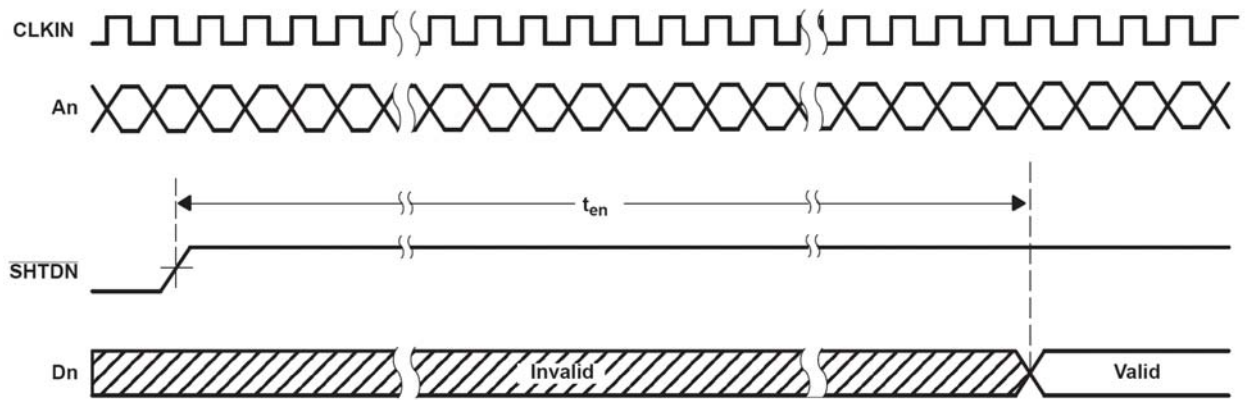


Figure 7. Enable Time Waveforms

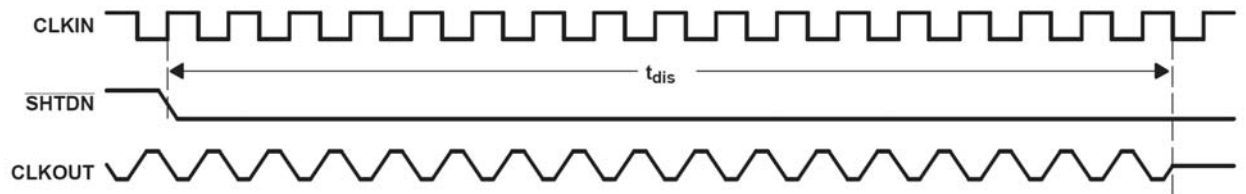


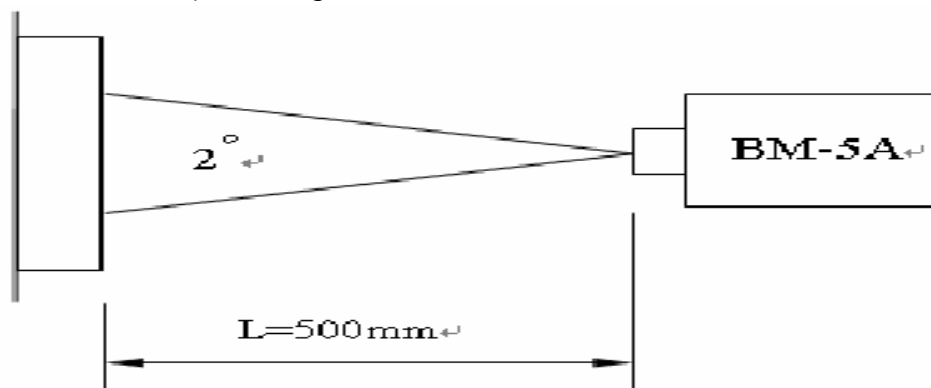
Figure 8. Disable Time Waveforms

8. OPTICAL CHARACTERISTICS

Item			Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast ratio			CR	Point - 5 $\Theta = \Phi = 0^\circ$	200	250	--	--	(1)(2)(3)	
Luminance			Lw		--	400	-	cd/m ²	(1)(3)	
Luminance Uniformity			ΔL		70	75	-	%	(1)(3)	
Response Time (White – Black)			T _r + T _f		--	50	--	ms	(1)(3)(5)	
Viewing Angle	Vertical		Θ	CR $\geq$ 10 Point – 5	80	100	-	Deg.	(1)(2)(4)	
	Horizontal		Φ		120	140	-			
Color chromaticity			Red	Rx	Point - 5 $\Theta = \Phi = 0^\circ$	0.566	0.616	0.666	--	(1)(3)
				Ry		0.302	0.352	0.402		
			Green	Gx		0.308	0.358	0.408		
				Gy		0.518	0.568	0.618		
			Blue	Bx		0.096	0.146	0.196		
				By		0.086	0.136	0.186		
			White	Wx		0.296	0.346	0.396		
				Wy		0.328	0.378	0.428		

NOTE :

- (1) Measure conditions : $25^\circ\text{C} \pm 2^\circ\text{C}$, $60 \pm 10\% \text{RH}$ under 10Lux , in the dark room by BM-7TOPCON) ,viewing 2° , VCC=3.3V , VDD=3.3V



- (2) Definition of Contrast Ratio :

Contrast Ratio (CR) = (White) Luminance of ON $\div$ (Black) Luminance of OFF

- (3) Definition of Luminance :

Measure white luminance on the point 5 as figure9-1

Definition of Luminance Uniformity

Measure white luminance on the point 1 ~ 9 as figure9-1

$$\Delta L = [L(\text{MIN}) / L(\text{MAX})] \times 100\%$$

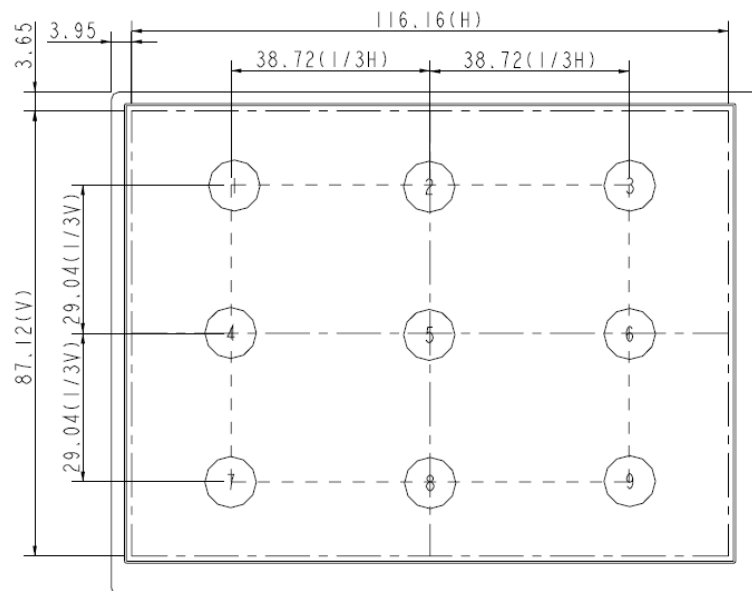


Fig9-1 Measuring point

(4) Definition of Viewing Angle(Θ , Φ), refer to Fig9-2 as below :

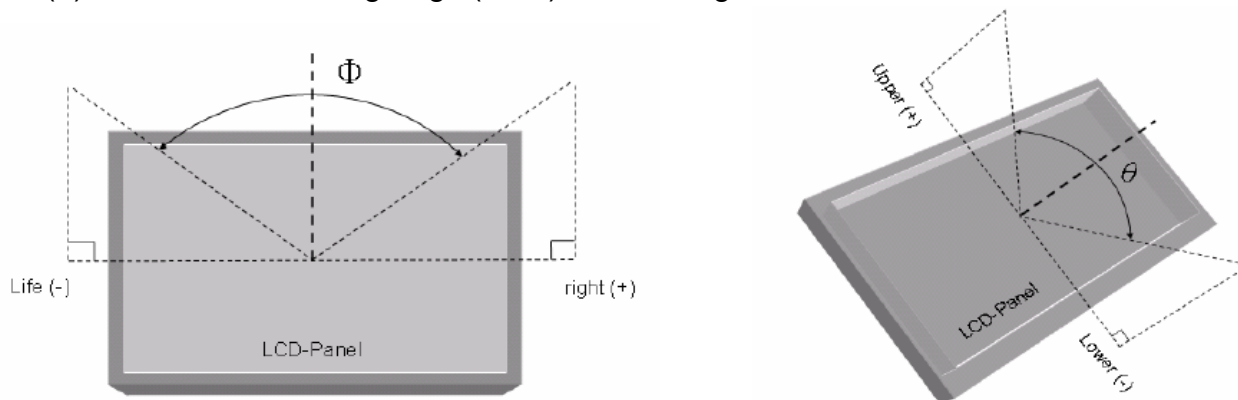


Fig9-2 Definition of Viewing Angle

(5) Definition of Response Time.(White – Black)

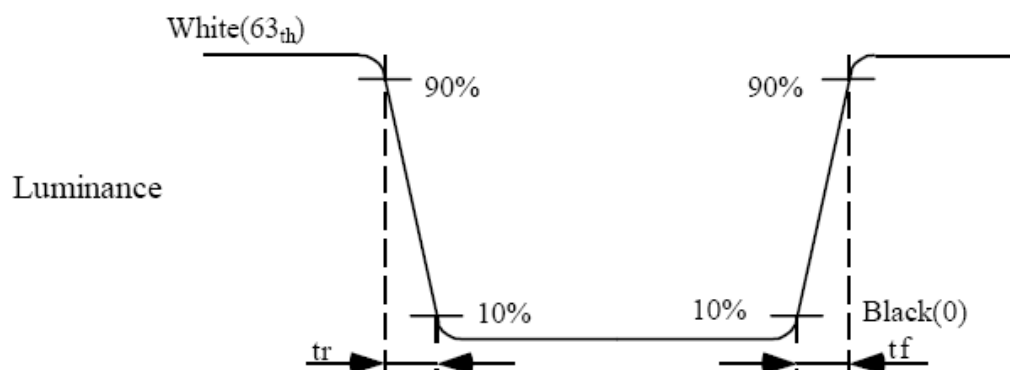


Fig9-3 Definition of Response Time(White-Black)

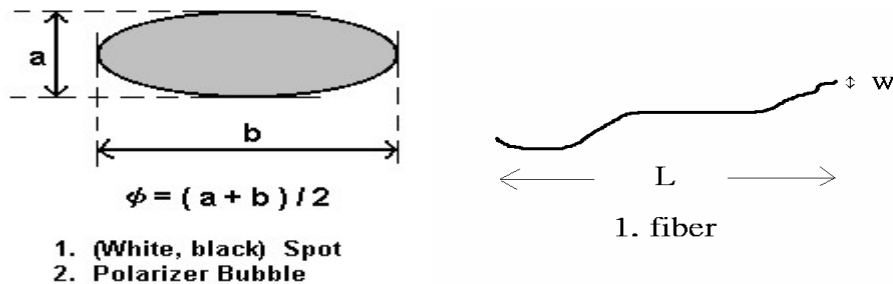
9. INCOMING INSPECTION STANDARD FOR TFT-LCD PANEL

DEFECT TYPE			LIMIT				Note		
VISUAL DEFECT	INTERNAL	SPOT	$\varphi < 0.15\text{mm}$		Ignore		Note1		
			$0.15\text{mm} \leq \varphi \leq 0.5\text{mm}$		$N \leq 4$				
			$0.5\text{mm} < \varphi$		$N = 0$				
		FIBER	$0.03\text{mm} < W \leq 0.1\text{mm}, L \leq 5\text{mm}$		$N \leq 3$		Note1		
			$1.0\text{mm} < W, 1.5\text{mm} < L$		$N = 0$				
		POLARIZER BUBBLE	$\varphi < 0.15\text{mm}$		Ignore		Note1		
			$0.15\text{mm} \leq \varphi \leq 0.5\text{mm}$		$N \leq 2$				
			$0.5\text{mm} < \varphi$		$N = 0$				
		Mura	It' OK if mura is slight visible through 6%ND filter						
ELECTRICAL DEFECT	BRIGHT DOT		A Grade			B Grade			
			C Area	O Area	Total	C Area	O Area	Total	Note3
			$N \leq 0$	$N \leq 2$	$N \leq 2$	$N \leq 2$	$N \leq 3$	$N \leq 5$	Note2
	DARK DOT		$N \leq 2$	$N \leq 3$	$N \leq 3$	$N \leq 3$	$N \leq 5$	$N \leq 8$	
	TOTAL DOT		$N \leq 4$			$N \leq 5$	$N \leq 6$	$N \leq 8$	Note2
	TWO ADJACENT DOT		$N \leq 0$	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	$N \leq 1$ pair	Note4
	THREE OR MORE ADJACENT DOT		NOT ALLOWED						
	LINE DEFECT		NOT ALLOWED						

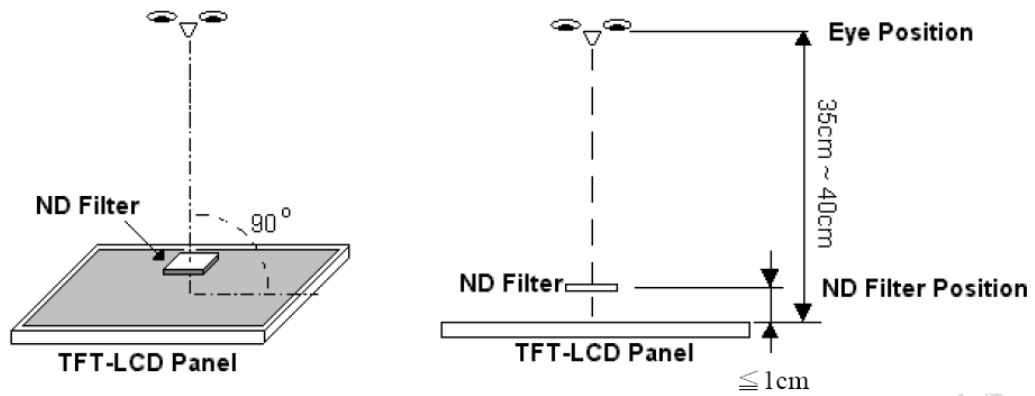
(1) One pixel consists of 3 sub-pixels, including R,G, and B dot.(Sub-pixel = Dot)

(2) LITTLE BRIGHT DOT ACCEPTITABLE UNDER 6 % ND-Filter

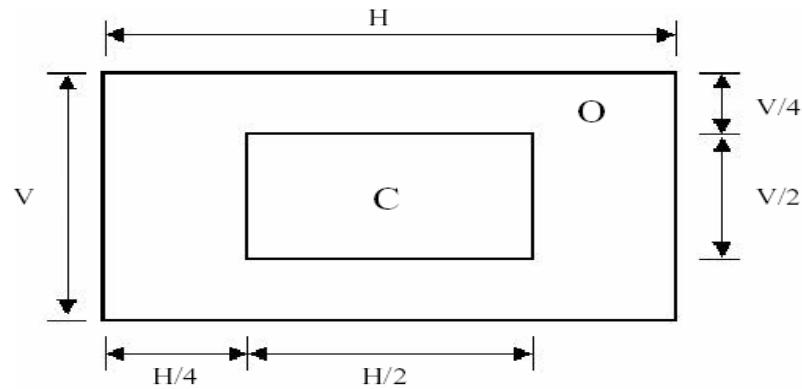
[Note1] W : Width[mm], L : Length[mm], N : Number, φ : Average Diameter



[Note2] Bright dot is defined through 6% transmission ND Filter as following.



[Note3]

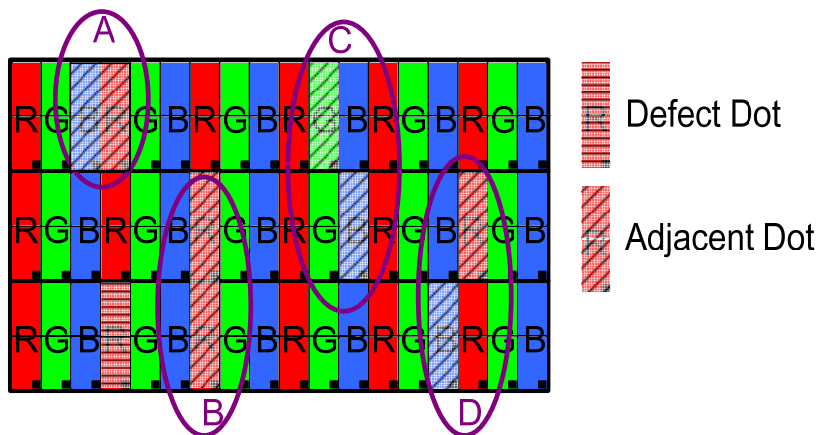


C Area: Center of display area

C Area: Outer of display area

[Note4]

Judge defect dot and adjacent dot as following. Allow below (as A, B, C and D status) adjacent defect dots, including bright and dark adjacent dot. And they will be counted 2 defect dots in total quantity.



- (1) The defects that are not defined above and considered to be problem shall be reviewed and discussed by both parties.
- (2) Defects on the Black Matrix, out of Display area, are not considered as a defect or counted.

10. RELIABILITY TEST CONDITIONS

ITEM	CONDITIONS
HIGH TEMPERATURE OPERATION	70°C , 240Hrs*
HIGH TEMPERATURE AND HIGH HUMIDITY OPERATION	40°C , 90%RH , 240Hrs*
HIGH TEMPERATURE STORAGE	80°C , 240Hrs*
LOW TEMPERATURE OPERATION	-20°C , 240Hrs*
LOW TEMPERATURE STORAGE	-30°C , 240Hrs*
THERMAL SHOCK	-30°C (0.5Hr) ~70°C (0.5Hr) * 50Cycle

Note* : After 24 hr room temp. and test .

10.1 OTHERS

AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

Technical Drawing of 640480G2-TAO TFT LCD

Top View Dimensions:

- Overall Width: 127.0 ± 0.3 (Bezel & T/P)
- Bezel Width (Left): 118.0 ± 0.2 (V,A) Bezel
- Bezel Width (Right): 115.2 (A,A)
- Module Width: 86.4 (A,A)
- Bezel Width (Bottom): 89.2 ± 0.2 (V,A) Bezel
- Module Width (Bottom): 98.4 ± 0.3

Side View Dimensions:

- Module Thickness: 9.1 ± 0.3
- Bezel Tape Removal: 20.0×10.0
- Stiffener Length: $L = 8.0$
- Contact Length: $L = 5.0$

Circuit Diagram:

Pin 1: YU(Y2)
 Pin 2: XL(X1)
 Pin 3: YD(Y1)
 Pin 4: XR(X2)

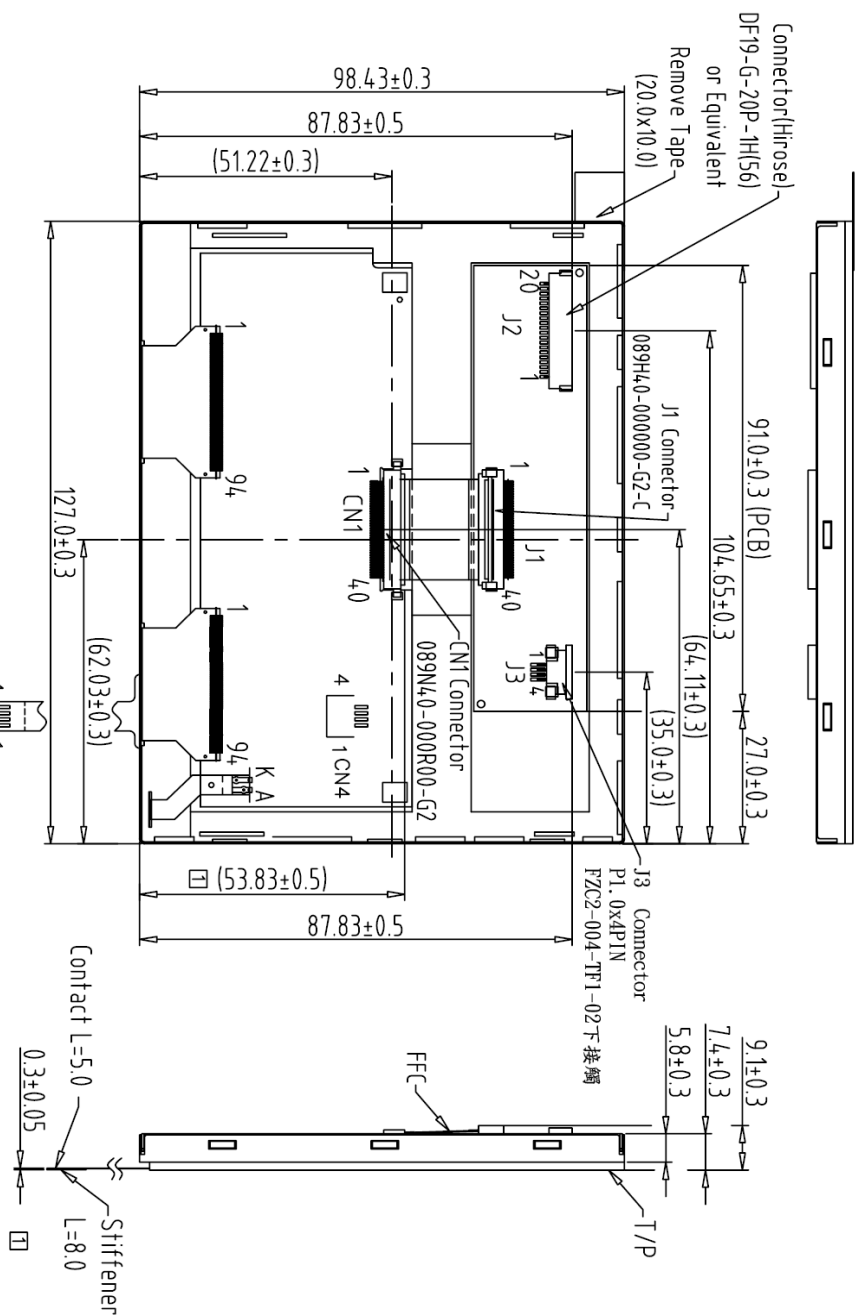
Note:

- Unless indicated, Tolerance Grade "B" is adopted.
- UV Glue For OLB Protection.
- LCD 640x480 (R,G,B) TFT LCD

Part Number: 640480G2-TAO

Manufacturer: 晶采光电科技 (Jingcai Optoelectronics Technology)

REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	07-24-09	SNOW
1	Modify 52, 83 to 53, 83 & T/P of PPC to Solder on PCB	08-27-09	SNOW



1	GND
2	VLED
3	VLED
4	ADJ

1	VDD	11	IN2-
2	VDD	12	IN2+
3	GND	13	GND
4	GND	14	CLK-
5	IN0-	15	CLK+
6	IN0+	16	GND
7	GND	17	NC
8	IN1-	18	NC
9	IN1+	19	GND
10	GND	20	GND

Back view

1	2	7	TOLERANCE GRADE(±)	A	B	DIAM.	MM	DWN.	SNOW	DATE	TITLE	DWG. NO.	SHEET
2		8	~6	0.05	0.1					07-24-09	640480G2-TA0		
3		9	6~18	0.08	0.18	IE NO.		CHK.		DATE	(5.7")	1	
4		10	18~50	0.1	0.25								
5		11	50~180	0.2	0.4	PARTS NO. LCM-1	APPD.			DATE			
6		12	180~	0.3	0.5	640480G2-TA0							