

## 晶采光電科技股份有限公司 AMPIRE CO., LTD.

# SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-19201080ITZQW-00H
APPROVED BY	
DATE	V-1-NpE

- ☐ Preliminary Specification
- Formal Specification



APPROVED BY	CHECKED BY	ORGANIZED BY
Kokai	Mark	Lawlite

This Specification is subject to change without notice.

## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2022/05/03 2023/03/06	 6,14,19 20	New Release Update Timing & LED connector & interface & drawing	Lawlite Lawlite

#### 1.0 General Descriptions

#### 1.1 Introduction

The LCM is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 15.6 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical Stripe and this module can display 16.7M colors(6bit+FRC). The TFT-LCD panel used for this module is a low reflection and higher color type.

#### 1.2 Features

- 3.3 V Logic Power
- LVDS (2ch) Interface for 1920 RGB x 1080 resolution
- 16.7M Colors(6bit+FRC)
- Green Product (RoHS)

## 1.3 Product Summary

Items	Specifications	Unit
Screen Diagonal	15.6	Inch
Active Area	344.16 (H) ×193.59 (V)	mm
Pixel Format	1920 (H) x RGB x 1080 (V)	J- C
Pixel Pitch	0.17925 (H) X 0.17925 (V)	mm
Pixel Arrangement	R.G.B. Vertical Stripe	-
Display Mode	Normally Black	-
White Luminance	1000 (Тур)	cd /m2
Contrast Ratio	1000 : 1 (Typ)	-
Input Voltage	3.3	V
Outline Dimensions	363.8x215.9x9.32(Max.)	mm
Support Color	16M(6Bit+FRC)	-

## 2.0 Absolute Maximum Ratings

ITEM	SYMBOL	VALUES			REMARK	
I I LIVI	STWIDOL	MIN	MAX	UNIT	ILIVIAIN	
Logic Supply Voltage	VDD	-0.3	4.0	V		
Operation Temperature	T <sub>op</sub>	-30	85	$^{\circ}$		
Storage Temperature	T <sub>st</sub>	-30	85	$^{\circ}$		

Note (1) Ta =25±2°ℂ



#### 3.0 ELECTRICAL SPECIFICATIONS

#### 3.1 LCD ELECTRONICS SPECIFICATION

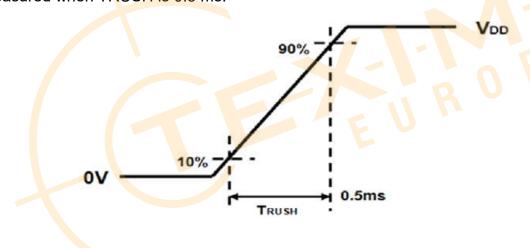
Parameter		Symbol		Unit	Note		
		Syllibol	Min	Тур.	Max.	Offic	Note
Power Supply V	oltage	VDD	3.0	3.3	3.6	V	(1),(2)
Ripple Volta	ge VRP		-	-	200	mV	(1),(3)
Rush Curre	nt	IRUSH	-	-	1.5	Α	(1),(4)
VDD	White	IDD			0.454	^	
Current	Pattern	טטו	_	_	0.454	Α	(1) (2)
VDD Power	White	PDD			1.5	W	(1),(3)
Consumption	Pattern	רטט	_	-	1.5	VV	

Note (1) All of the specifications are guaranteed under normal conditions. Normal conditions are defined as follow: Temperature: 25°C , Humidity: 55± 10%RH.

Note (2) All of the absolute maximum ratings specified in the table, if exceeded, may cause faulty operation or unrecoverable damage. It is recommended to follow the typical value.

Note (3) The specified VDD current and power consumption are measured under the VDD = 3.3 V, FV = 60 Hz condition and White Pattern.

Note (4) The figures below is the measuring condition of VDD. Rush current can be measured when TRUSH is 0.5 ms.



## 4. Interface Timings

#### **4.1 DISPLAY TIMING SPECIFICATIONS**

Parameter	Symbol	Min.	Тур.	Max.	Unit
LVDS Clock Frequency	Fclk	(69.5)	(70.5)	(73)	MHz
V Total Time	VT	(1104)	(1116)	(1080+A)	Clocks
VActive Time	VA		1080		-
H Total Time	HT	(1050)	(1052)	(960+B)	Lines
H Active Time	HA		960		9 -
Frame Rate	FV	-	(60)	-	Hz

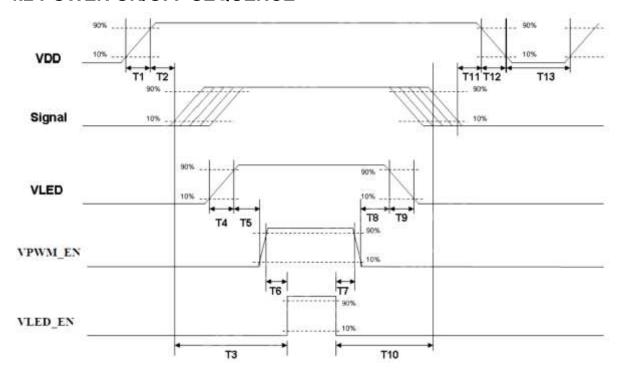
Note (1) SCC can only be driven to 2%

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Note (2) The maximum clock frequency=[(960+B)\*(1080+A)\*60]<73MHz



## **4.2 POWER ON/OFF SEQUENCE**



Power Sequencing Requirements

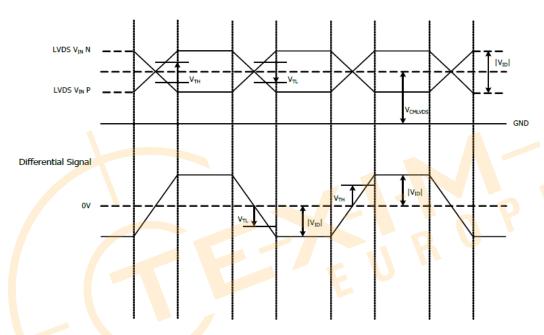
Parameter	Min.	Тур.	Max.	Unit
T1	0.5	-	10	ms
T2	30	40	50	ms
T3	200	-	-	ms
T4	0.5	- /	10	ms
T5	10	-	-	ms
T6	10	-		ms
T7	0	ı	-	ms
T8	10	ı	-	ms
Т9	_	ı	10	ms
T10	110	ı	-	ms
T11	0	16	50	ms
T12	-	-	10	ms
T13	1000	-	-	ms

## 4.3 LVDS INPUT SIGNAL SPECIFICATIONS

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Differential Input High Threshold	Vth	ı	-	+100	mV	VCM=1.2V
Differential Input Low Threshold	Vtl	-100	-	-	mV	VCM=1.2V
Magnitude Differential Input Voltage	[VID]	100	-	600	mV	-
Common Mode Voltage	VCM	0.7		1.6	V	-

Note (1) Input signals shall be low or Hi- resistance state when VDD is off.

Note (2) All electrical characteristics for LVDS signal are defined and shall be measured at the interface connector of LCD.

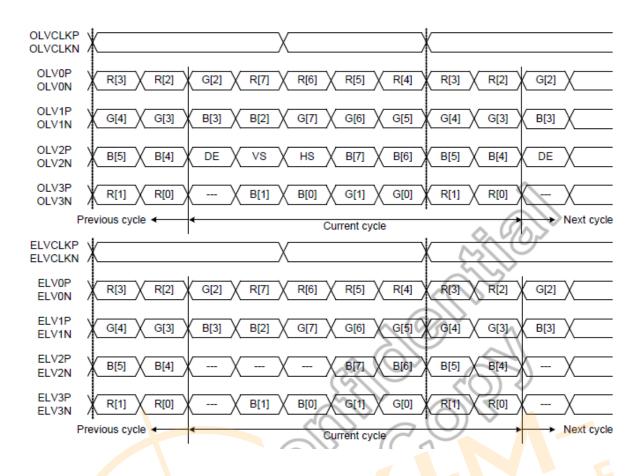


LVDS AC Electrical Characteristics

Parameter Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock Period	TLVCP	-	(T)	-	ns
Clock High Time	TLVCH	-	(4T/7)	-	ns
Clock Low Time	TLVCL	-	(3T/7)	-	ns

Note : T=1/Fclk

## **Data Mapping**

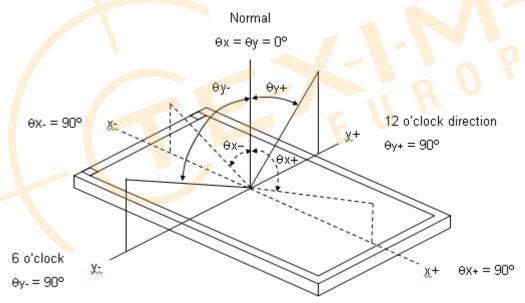


## **5.0 Optical Specifications**

The optical characteristics are measured under stable conditions as following notes

Iter	m	Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Red	Rx			T.B.D			
	Red	Ry			T.B.D			
	Green	Gx			T.B.D			
Color Chromaticity	Orcen	Gy	$\theta_x=0^\circ$ , $\theta_Y=0^\circ$	Тур –	T.B.D	Typ +		(1) (5)
(CIE 1931)	Blue	Bx	CS-2000	0.05	T.B.D	0.05	_	(1), (5)
(0.2 .00.)	Blue	Ву	00 2000		T.B.D			
	\\/hito	Wx			0.313			
	White	Wy			0.329			
Center Lumina	nce of White	L <sub>C</sub>		800	1000	-	cd/m <sup>2</sup>	(4), (5)
Contras	Contrast Ratio			700	1000	-	-	(2), (5)
Respons	e Time	$T_{R+}T_{F}$	$\theta_x=0^\circ$ , $\theta_Y=0^\circ$	-	25	35	ms	(3)
NTS	SC		$\theta_x=0^\circ$ , $\theta_Y=0^\circ$	-	72	•	%	(5), (6)
Luminance	Uniformity		9 Points	75	80		%	(5), (6)
	Horizontal	θ <sub>x</sub> +		80	85			
Viewing Angle		θ <sub>x</sub> -	CR ≧ 10	80	85		Deg.	(1), (5)
Viewing Angle	Vertical	θ <sub>Y</sub> +	OIX ≦ 10	80	85		Deg.	(1), (3)
	vertical	θ <sub>Y</sub> -		80	85			

Note (1) Definition of Viewing Angle ( $\theta x$ ,  $\theta y$ ):



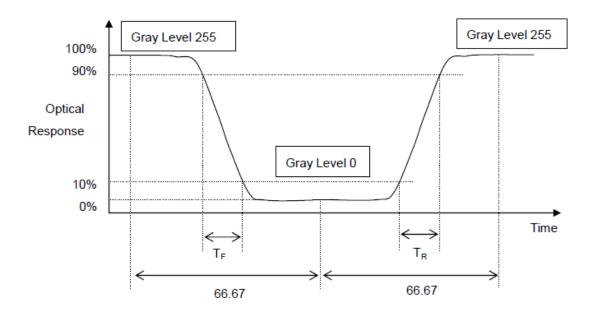
Note (2) Definition of Contrast Ratio (CR):

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The contrast ratio can be calculated by the following expression. Contrast Ratio (CR) = L255 / L0 L255: Luminance of gray level 255 L 0: Luminance of gray level 0 CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time  $(T_R, T_F)$ :



Note (4) Definition of Luminance of White (L<sub>C</sub>):

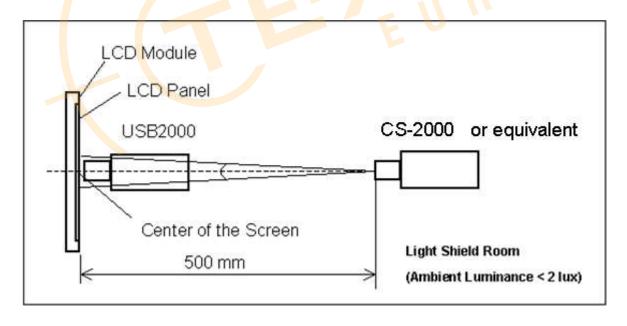
Measure the luminance of gray level 255 at center point  $L_C = L(5)$ 

L (x) is corresponding to the luminance of the point X at Figure in Note (6).

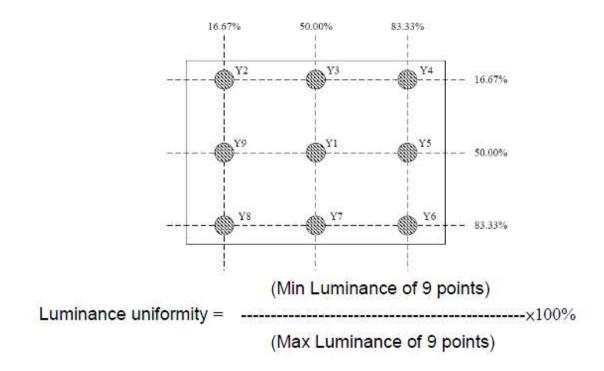
Note (5) Measurement Setup:

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The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (6) Definition of White Variation





## 6. Interface Connections

## **LVDS** connector

Item	Description
Manufacturar / Type	STM
Manufacturer / Type	MSBKT2407P30HB
Mating Receptacle / Type (Reference)	JAE
Mating Receptacle / Type (Reference)	FI-X30HL(Locked Type)

Pin	Name	Description
1	RxO0-	Negative LVDS differential data input (Odd data)
2	RxO0+	Positive LVDS differential data input (Odd data)
3	RxO1-	Negative LVDS differential data input (Odd data)
4	RxO1+	Positive LVDS differential data input (Odd data)
5	RxO2-	Negative LVDS differential data input (Odd data)
6	RxO2+	Positive LVDS differential data input (Odd data)
7	GND	Ground
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)
10	RxO3-	Negative LVDS differential data input (Odd data)
11	RxO3+	Positive LVDS differential data input (Odd data)
12	RxE0-	Negative LVDS differential data input (Even data)
13	RxE0+	Positive LVDS differential data in <mark>pu</mark> t (Eve <mark>n d</mark> ata)
14	GND	Ground
15	RxE1-	Negative LVDS differential data input (Even data)
16	RxE1+	Positive LVDS differential dat <mark>a i</mark> nput (Eve <mark>n</mark> data)
17	GND	Ground
18	RxE2-	Ne <mark>gati</mark> ve LVD <mark>S differen</mark> tial data inp <mark>ut</mark> (Even data)
19	RxE2+	Posit <mark>ive</mark> LVDS differential data input (Even data)
20	RxECLK-	Negative LVDS differential clock input (Even clock)
21	RxECLK+	Positive LVDS differential clock input (Even clock)
22	RxE3-	Negative LVDS differential data input (Even data)
23	RxE3+	Positive LVDS differential data input (Even data)
24	GND	Ground
25	Bist	LCD Panel Self Test Enable(3.3V Typ.) For Ampire use, When
		it is not used, Connecting to GND or Floating is recommended
26	SDA	I2C-Compatible Serial-Data Input. Floating is recommended in the Costumer
27	SCL	I2C-Compatible Serial-Data Input. Floating is recommended in the Costumer
28	VDD	Power Supply Input Voltage(3.3V)
29	VDD	Power Supply Input Voltage(3.3V)
30	VDD	Power Supply Input Voltage(3.3V)

## **LED connector**

Item	Description
Manufacturer / Type	STM/MSB24038P8A(P1.25x8pin)

Pin	Name	Description
1	VLED	LED power supply(12V Typ)
2	VLED	LED power supply(12V Typ)
3	VLED	LED power supply(12V Typ)
4	GND	Ground
5	GND	Ground
6	GND	Ground
7	VLED_EN	Backlight On/Off (3.3V Typ)
8	VPWM_EN	System PWM signal imput for dimming (3.3V Typ)

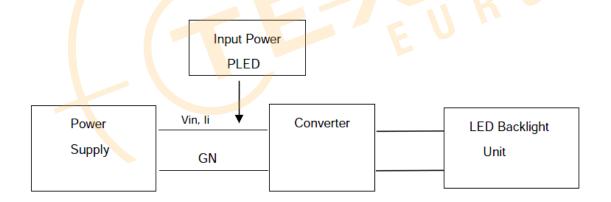


## 7. LED Driving Conditions

Dorom	Symbol	Value			Linit	Note	
Parameter		Min.	Тур.	Max.	Unit	Note	
Converter Power	LED_Vin	10.8	12.0	13.2	V		
Converter Power	li			2	Α	@LED_Vin= 12V Duty=100%	
Power Con	P <sub>LED</sub>			26.3	W	@ LED_Vin = 12V Duty=100%	
EN Control Level	Backlight on	LED EN	1.5	-	5.5	V	
	Backlight off		0		0.5		
PWM Control Level	PWM High Level	LED_PWM	1.2	-	5.5	٧	
	PWM Low Level		0	-	0.5		
PWM Control	PWM	10		100	%		
PWM Control	f <sub>PWM</sub>	100	-	1000	Hz		
LED Life	L <sub>L</sub>	50,000			Hrs	(2)	

Note (1) LED light bar input voltage and current are measured by utilizing a true RMS multimeter as shown below:

Note (2) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at Ta = 25±2°C and Duty 100% until the brightness becomes ≤ 50% of its original value. Operating LED under high temperature environment will reduce life time and lead to color shift.



## 8. Reliability Test

The reliability test items and its conditions are shown below.

Test Item	Test Conditions	Note
High Temperature Operation	85±3°C , t=240 hrs	
Low Temperature Operation	-30±3°C , t=240 hrs	
High Temperature Storage	85±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	40°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 60°C (30min), 100 cycles	1,2
Shock (Non-Operating)	50G, 11ms, half sine wave, 1 time for ± X, ± Y, ± Z.	
Vibration (Non-Operating)	1.5G, 10 ~ 300 Hz, 10min/cycle, 3 cycles each X, Y, Z	

Note (1) Condensation of water is not permitted on the module.

- Note (2) The module should be inspected after 1 hour storage in normal conditions (15-35°C, 45-65%RH).
- Note (3) The module shouldn't be tested more than one condition, and all the test conditions are independent.
- Note (4) All the reliability tests should be done without protective film on the module.

## 9. GENERAL PRECAUTION

#### 9.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

#### 9.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. AMPIRE does not warrant the module, if customers disassemble or modify the module.

#### 9.3 Breakage of LCD Panel

- (1) If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- (2) If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- (3) If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- (4) Handle carefully with chips of glass that may cause injury, when the glass is broken.

#### 9.4 Electric Shock

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- (1) Disconnect power supply before handling LCD module.
- (2) Do not pull or fold the LED cable.
- (3) Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

#### 9.5 Absolute Maximum Ratings and Power Protection Circuit

- (1) Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- (2) Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- (3) It's recommended to employ protection circuit for power supply.

#### 9.6 Operation

- (1) Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- (2) Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- (3) When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- (4) Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may cause deformation or color fading.
- (5) When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

#### 9.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

#### 9.8 Static Electricity

- (1) Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- (2) Because LCD modules use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

#### 9.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 9.10 Disposal

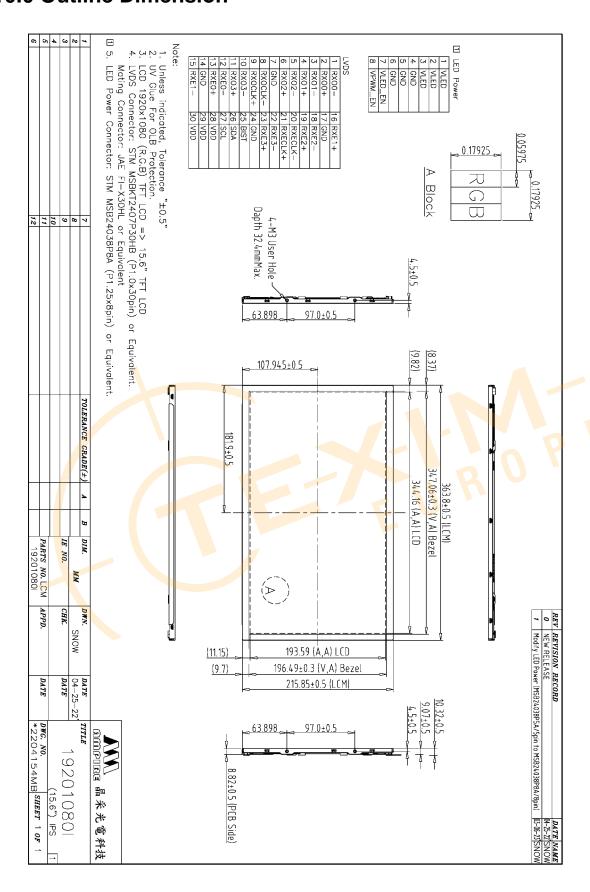
When disposing LCD module, obey the local environmental regulations.

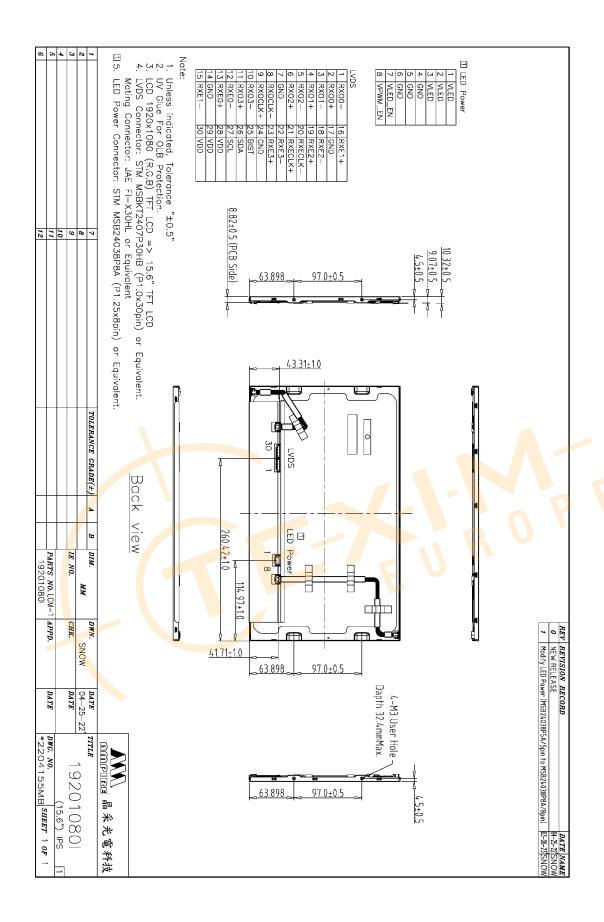
#### 9.11 Others

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Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.

## 10.0 Outline Dimension





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Please contact us if you have any questions about the contents of the datasheet.

This may not be the latest version of the datasheet. Please check with us if a later version is available.



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