



# **HDMI TFT Module Specification**

# MODEL: HA-150GVEBCBA0-V



< >> PRELIMINARY SPECIFICATION

<>> APPROVAL SPECIFICATION

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# **RECORD OF REVISION**

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## 1. GENERAL DESCRIPTION

## 1.1 Description

HA-150GVEBCBA0-V is a 15.0 (4:3) inch diagonally measured active display with high resolution XGA 1024x768 display and high brightness. This model is composed of a TFT LCD panel, backlight system, a projected capacitive touch panel and HDMI. It is designed to make Raspberry Pi usage easy. You can simply use this TFT display with your Raspberry Pi, or also you can use this as computer display with any device which has HDMI output. This 15.0" TFT model comes in 1024x768 resolution that would be great for embedded computing usage too.

## 1.2 Features:

No.	ltem	Specification	Unit
1	Panel Size	15"	Inch
2	Number o <mark>f</mark> Pixels	1024 (H) x RGB x 768 (V)	Pixels
3	Active Area	304.1 (H) x 228.1 (V)	mm
4	Pixel Pitch	0.297 (H) x 0.297 (V)	mm
5	Outline Dimension	341.11 (H) × <mark>26</mark> 5.1 ( <mark>V)</mark> × 27.1 (T)	mm
6	Number of Colors	16.7M	
7	Display Mode	MVA / Normally Black / Transmissive	
8	View Direction	Free direction	
9	Display <mark>F</mark> ormat	RGB vertical stripe	
10	Surface Treatment	Clear (≧6H)	
11	Contrast Ratio	2500 (Typ.)	
12	Luminance (cd/m^2)	450 (Typ.)	cd/m2
13	Video Input Interface	HDMI	
13		(Compliance HDMI V1.4)	
14	Backlight	White LED	
15	Operation Temperature	-20 ~ 70	°C
16	Storage Temperature	-30 ~ 80	°C
17	Weight	(TBD)	g



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## 2. MECHANICAL SPECIFICATION



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#### 3. PIN DESCRIPTION

3.1 Po	<b>3.1 Power Input(CN1)</b> [DC JACK:SCD480CCS000B00GE or c			ompatible]
Pin No.	Symbol	1/0	Function	Note
1	12V	Ρ	Power Supply +12V	12.0V
2	GND	Ρ	Ground	

## **3.2 Back-light Control(CN2)** [WAFER P2.0mm:2001S-03-RTE or compatible]

Pin No.	Symbol	I/0	Function	Note
1	GND	Р	Ground	
2	N.C.	-	N.C.	
3	PWM		Back-light Dimming control (internal pull up to 3.3V)	*1

\*1: When PWM not connected, back-light default is typical brightness.

## 3.3 HDMI (CN3) 🐧

[HDMI A TYPE:PHD0911A2301E or compatible]

	· /			
Pin No.	Symbol	1/0	Function	Note
1	TMDS 2+		TMDS Data2+	
2	GND	P	TMDS Data2 Shield	
3	TMDS 2-		TMDS Data2-	
4	TMDS 1+	Ι	TMDS Data1+	
5	GND	Ρ	TMDS Data1 Shield	
6	TMDS 1-	Ι	TMDS Data1-	
7	TMDS 0+	Ι	TMDS Data0+	
8	GND	Р	TMDS Data0 Shield	
9	TMDS 0-	_	TMDS Data0-	
10	TMDS CLK+	I	TMDS Clock+	
11	GND	Р	TMDS Clock Shield	
12	TMDS CLK-	I	TMDS Clock-	
13	N.C.	-	N.C.	
14	N.C.	-	N.C.	
15	DDC_SCL		IIC SCL to EDID ROM	
16	DDC_SDA	I/0	IIC SDA to EDID ROM	
17	GND	Р	DDC/CEC Ground	
18	HD_5V	Р	+5V Power	
19	HPD	0	Hot Plug Detect	



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## 4. ABSOLUTE MAXIMUM RATINGS

## 4.1 Electrical Absolute Rating

## 4.1.1 HDMI TFT LCD Module

ltom	Symbol	Val	ues	Unit	Note
ltem	Symbol	Min	Max.	Unit	
Power supply voltage	12V	10	14	V	

## 4.1.2 Environment Absolute Rating

ltom	Symbol	Values			Unit	Noto
ltem	Symbol	Min	Тур	Max.	Unit	Note
Operating Temperature	Тор	-20	-	70	°C	Ambient
Storage Temperature	Tst	-30	-	80	°C	temperature





- 5. BLOCK DIAGRAM
  - 5.1 TFT LCD Module





## 6. ELECTRICAL CHARACTERISTICS

## 6.1 HDMI TFT LCD Module

ltem	Symbol		Values	Unit	Note	
item	Symbol	Min	Тур.	Max.	Unit	NOLE
Supply Voltage	12V	11	12	13	V	
PWM frequency		190	200	20K	Hz	
PWM Duty		1	-	100	%	
PWM Dimming	Vpwm-ih	3.0	3.3	5	۷	
Voltage	VPWM-IL	0	-	0.8	V	
Supply Current	ICC(12V)	-	TBD	-	mA	
LED life time		50000	70000	-	Hr	(1)

Note 1:

The "LED life time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is  $25^{\circ}$ C 60% RH.



## 7. PROJECTED CAPACITIVE PANEL SPECIFICATIONS

## 7.1 Main Feature

Item	Specification	Unit
Screen Size	15 inches	Diagonal
Туре	Transparent Type Projected Capacitive Touch Panel	
Input Mode	Human's Finger	
Interface	I2C or USB	
Touch number	10 points	
Cover glass pencil-hardness	<u>≧</u> 6H	
Bonding	UV-CUT OCA	
Response time	≤25ms	ms
Controller IC	ILI2511	

## 7.2 PCT Control:IIC (CN3) [WAFER P1.25mm:50271-0060L-002 or compatible]

Pin No.	Symbol	I/0	Function	Note
1	GND	Р	Ground	
2	VDD	Р	Power supply for IIC	
3	SCL	I	IIC SCL to PCT Controller	
4	SDA	1/0	IIC SDA to PCT Controller	
5	INT	0	Interrupt	
6	RESET		Reset	

## 7.3 PCT Control:USB (CN4) [WAFER P1.25mm:50271-0050L-002 or compatible]

Pin No.	Symbol	1/0	Function	Note
1	GND -EARTH	Р	Earth Ground(Shield)	
2	VDD_5V	Р	Power supply for USB I/F	
3	GND	Р	Power Ground	
4	D+	1/0	USB data +	
5	D-	1/0	USB data -	

## 7.4 PCT Control: FPC

Pin No.	Symbol	I/0	Function	Note
1	GND	Р	Ground	
2	VDD	Р	Power supply for I2C	3.3V
3	SCL	I	IIC SCL to PCT Controller	
4	SDA	1/0	IIC SDA to PCT Controller	
5	INT	0	Interrupt signal to inform the host processor that touch data is ready for read	
6	RESET	I	External low signal reset the chip.	
7	VDD_5V	Р	Power supply for USB I/F	
8	D+	1/0	USB data +	
9	D-	1/0	USB data -	
10	GND	Р	Ground	



## 8. OPTICAL CHARACTERISTICS

ltem		Symbol	Condition	Min.	Тур.	Max.	Unit
Brighti	ness			360	450		cd/m2
Contrast Ratio		CR		1800	2500		
		Tr			16	21	ms
Response	e nine	Tf			7	14	ms
	White	Wx	Note1, Note 3,	0.263	0.313	0.363	
	White	Wy	$(\theta = 0^{\circ},$	0.279	0.329	0.379	
	Red	Rx	Normal	0.597	0.647	0.697	
Color		Ry	Viewing Angle)	0.288	0.338	0.388	
Chromaticity	Green	Gx	Angle)	0.271	0.321	0.371	
		Gy		0.556	0.606	0.656	
	Dhue	Bx		0.107	0.157	0.207	
	Blue	Ву		0.000	0.039	0.089	
	Horizontal	heta x+		80	88		
View and a		θ <b>x-</b>	Center	80	88		
View angle	Vertical	θ <b>Y</b> +	⊂ CR≥10	80	88		
		θ <b>Υ-</b>		80	88		E

Note : The following optical specifications shall be measured in a darkroom or equivalent state(ambient luminance  $\leq 1 \text{ lux}$ , and at room temperature). The operation temperature is 25°C±2°C. The measurement method is shown in Note1.

Note1: The method of optical measurement:



Note2: Measured at the center area of the panel and at the viewing angle of the  $\theta x = \theta y$ =0°



Note3: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state  $\div$  Luminance with all pixels in Black state

## Note 4: Definition of Viewing Angle:



Note 5: Definition of Brightness Uniformity (B-uni):



B-uni = (Minimum luminance of 9 points÷Maximum luminance of 9points)X100%



Note 6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time (Tr)" and the "Falling Time (Tf)" respectively. Tr and Tf are defined as following figure



Note 7: Definition of Chromaticity:

The color coordinates (Wx,Wy),(Rx,Ry),(Gx,Gy),and (Bx,By) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.





## 9. RELIABILITY

## 9.1 Test Condition

9.1.1 Temperature and Humidity(Ambient Temperature) Temperature :  $25 \pm 5^{\circ}$ C Humidity :  $65 \pm 5^{\circ}$ 

# 9.1.2 OperationUnless specified otherwise, test will be conducted under function state.

9.1.3 ContainerUnless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

## 9.1.4 Test Frequency In case of related to deterioration such as shock test. It will be conducted only once.

7.2	12515	
No.	ITEM	CONDITION CRITERION
1	High Temperature Storage	80°C, 120 hrs
2	Low Temperature Storage	-30°C, 120 hrs
3	High Temperature Operating	70°C, 120 hrs
4	Low Temperature Operating	-20°C, 120 hrs
5	Hig <mark>h</mark> Temperature/Humidity Non-Operating	40°C, 90%RH, 120 hrs
6	Temperature Shock Non-Operating	-30°C $\leftrightarrow \rightarrow$ 80°C (0.5hr each), 100 cycles
7	Vibration Test Non-Operating	Frequency:0 ~ 55 Hz Amplitude:1.5 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z
8	Electro-static Discharge	$\pm$ 2KV, Human Body Mode, 100pF/1500 $\Omega$

## 9.2 TESTS

Note1: The test sample have recovery time for 24 hours at room temperature before the function check. In the standard conditions, there is no any function NG issue occurred.



## 9.3 JUDGMENT STANDARD

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.





## 9.4 INCOMING INSPECTION STANDARDS

No.	Parameter					Criteria				
							ction (Ma	jor)		
			st ratio (	· · · · ·		/				
		Does n	ot meet	specifi	ed ra	ange in th	ne spec. (	Major)	(Note:3)	
		Line De					d Horizon		defect in	bright,
			dark and colored. (Major) (Note:1)							
		Point D	)efect : /	Active a	-		(Minor) (N	lote:1)		
			lte	-	Acc	eptable n	umber	Tota		
			ne	ern -		Active A	rea	Tota	1	
			Brig	aht		2				
			Da	-		4		5		
			Da			7				
1	Operating						-			
							ND filter. (			
		Foreig	n mater	ial in B	ack	or White	spots sha		>1/4L)	
			$\sim$	Zone	Acc	eptable	Class	s	AQL	
						umber	Of		Level	
			Dimens				Defec	is		_
			D>			0				
			0.3 < D			5	Mino	r	1.5	
			D≤			*				
			D = (Loi				Disregard			
		Foreig	in Mater				hape (W≤	(1/4L) (	Note: 4)	_
					Zone	Ac	ceptable	Class	S AQL	
				144			umber	Of		
		L (mr		W(mr				Defec	ts	
			_ >5		V>0.		0		15	
			< L ≤ 5				5	Mino	r 1.5	
			<u>≤0.5</u>	_	<u>≤0.0</u>					
		L :	Length		Widt		isregard			
			nsion: O				\			
			appear ch on th			en (Minor	)			
					20. ( 'one	Accepta	Clas		AQL	
					.one	ble	Of Def			
			(mm)	W(mm		number		0013	2010	
			(1111)	W>0			Mino	or	1.5	—
			 L ≤ 3	V>0 W≤0		3	IVIIII		1.5	
			LZJ	VVSU	. 1	3				
	Extornal Increation			\A/ -	بلہ:/۸	h	orogerd			
2	External Inspection (non-operating)		Length			th   ∗ : Di irize (Not				
2	(non-operaulig)						e.2) Class			
			20	10		eptable	Of	AQ		
		Г	Dimensio	on	n	umber	Defects	Lev	/el	
			D≤0	>		*			_	
			0 D≤0			3	Minor	1.	5	
						-		-		
		D	= (Long	+ Shor	t) / 2		∗ : Disr	regard		
			(3			-				



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			Definition
Class of	Major		It is a defect that is likely to result in failure or to reduce materially the
defects			usability of the product for the intended function.
defects	Minon	AQL 1.5%	It is a defect that will not result in functioning problem with deviation
	Minor		classified.

Note1:

(a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively (b)Dark point defect is defined as visible in full white pattern.

(c)Definition of distribution of point defect is as follows:

-minimum separation between dark point defects should be larger than 5mm.

-minimum separation between bright point defects should be larger than 5mm.

(d)Definition of joined bright point defect and joined dark point defect are as follows:

-Two or more joined bright point defects must be nil.

- -Three joined dark point defects must be nil.
- -Coupling of one dark and one bright point in junction is counted as one dark and bright spot with 1 pair maximum.
- -Two Joined dark point is counted as two dark points with 2 pair maximum.

Note2: The external inspection should be conducted at the distance 30± 5cm between the eyes of inspector and the panel.

Note3: Luminance measurement for contrast ratio is at the distance  $50\pm$  5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note4: W-Width in mm , L-length of Max.(L1,L2) in mm.





## 9.5 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

Sampling table: MIL-STD-105E

Inspection level: Level II

## 9.6 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.

 $\theta {\leq} 45^{\circ}$  inspection under non-operating condition.

 $\theta \leq 5^{\circ}$  inspection under operating condition





- 10. PRECAUTION RELATING PRODUCT HANDLING
  - 10.1 SAFETY
  - 10.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
  - 10.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.
  - 10.2 HANDLING
  - 10.2.1 Avoid any strong mechanical shock which can break the glass.
  - 10.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
  - 10.2.3 Do not remove the panel or frame from the module.
  - 10.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, Do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
  - 10.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
  - 10.2.6 Do not touch the display area with bare hands , this will stain the display area.
  - 10.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
  - 10.2.8 To control temperature and time of soldering is 280 ± 10°C and 3-5 sec.
  - 10.2.9 To avoid liquid (include organic solvent) stained on LCM.
  - 10.3 STORAGE
  - 10.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
  - 10.3.2 Do not place the module near organics solvents or corrosive gases.
  - 10.3.3 Do not crush, shake, or jolt the module.

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