

深圳市一众显示科技有限公司

SHEN ZHEN TEAM SOURCE DISPLAY TECH. CO, LTD.

TFT-LCD Module Specification



Module NO.: TST028CMIL-02P

Version: V1.0

ATTROVAL FOR SI ECIFICATION	□ ALLKOVAL FOR SAMILLE

For Customer's Acceptance:	
Approved by	Comment
	RURU

Team Source Display:							
Presented by	Reviewed by	Organized by					

Version No.	Date	Content	Remark
V1.0	2012-5-18	Initial Release	



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1. SPECIFICATIONS

1.1 Features

Main LCD panel

Standard Value			
240*(R、G、B)*320 Dots			
a-si TFT,Positive,Transmissive type			
2.8" (Diagonal)			
6 O'Clock			
R.G.B. vertical stripe			
White LED B/L			
8080 8/16Bit data bus			
ILI9341			

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	50.00(W) *69.2 (L) *3.6max (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	43.2 (W) *57.6 (L)	mm

Touch panel

Item	Standard Value	Unit
Outline Dimension	49.5*68.75*1.2	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VCI	-	-0.3	+4.6	V
	VGH-VSS	-	-0.3	+18.5	V
	VSS-VGL	-	-0.3	+18.5	V
Input Voltage	V _{IN}	-	-0.3	VCI+0.3	V
Operating Temperature	T_OP	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C
Storage Humidity	H_D	Ta < 40°C	20	90	%RH

1.4 DC Electrical Characteristics

Module VSS= 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	VCI	-	2.6	2.8	3.3	٧
Input High Voltage	Vıн		0.8*VCI	A	VCI	V
Input Low Voltage	VIL		-0.3	V	0.2*VCI	V
Output High Voltage	Vон	- E U	0.8*VCI	-	-	V
Output Low Voltage	Vol	-	-	-	0.2*VCI	V
Supply Current	ICC	VCI =2.8 V Pattern=full display*1	-	TBD	-	mA

Note1:Maximum current display



1.5 Optical Characteristics

TFT LCD panel

VCC=2.8V, Ta=25°C

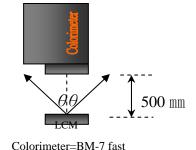
		•						
Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response tin	Response time		Ta = 25° C θ X, θ Y = 0°	-	30	-	ms	Note2
Contrast rati	o	CR			250	-	ı	Note3
	White	X		0.283	0.303	0.323		
	vviile	Y		0.305	0.325	0.345		
Color of CIE	Red	Х	To - 25°C	0.606	0.626	0.646	-	Note1
Color of CIE Coordinate	Reu	Υ	Ta = 25° C θ X , θ Y = 0°	0.314	0.334	0.354		
(With B/L)	Croon	Х		0.257	0.277	0.297		
(VVIIII D/L)	Green	Y		0.529	0.549	0.569		
	Blue	Х		0.122	0.142	0.162		
	Diue	Y		0.102	0.122	0.142		
Average Brighti Pattern=white di (main)		IV	IF= 60mA	-	150	-	cd/m ²	Note1
Uniformity		∆В	IF= 60mA	80	-	-	%	Note1

Note1:

- 1 : △B=B(min) / B(max) ×100% 2 : Measurement Condition for Optical Characteristics: a : Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency. b : Measurement Distance: $500 \pm 50 \, \text{mm}$, $(\theta = 0^{\circ})$ c : Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%

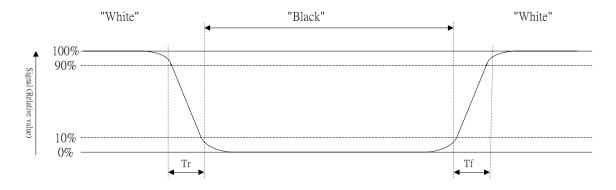




Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

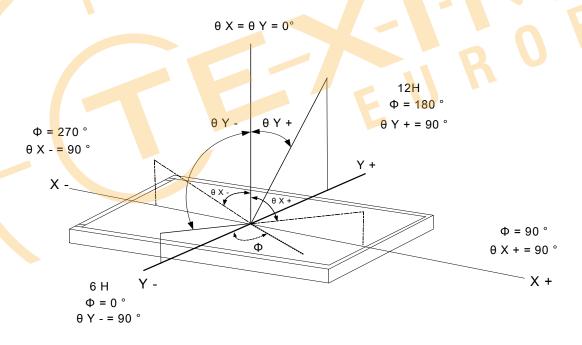
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight & LED Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	60	mA
Forward Voltage	VF	Ta =25°C	-	3.5	V
Power Dissipation	PD	Ta =25°C	-	210	mW

Electrical / Optical Characteristics

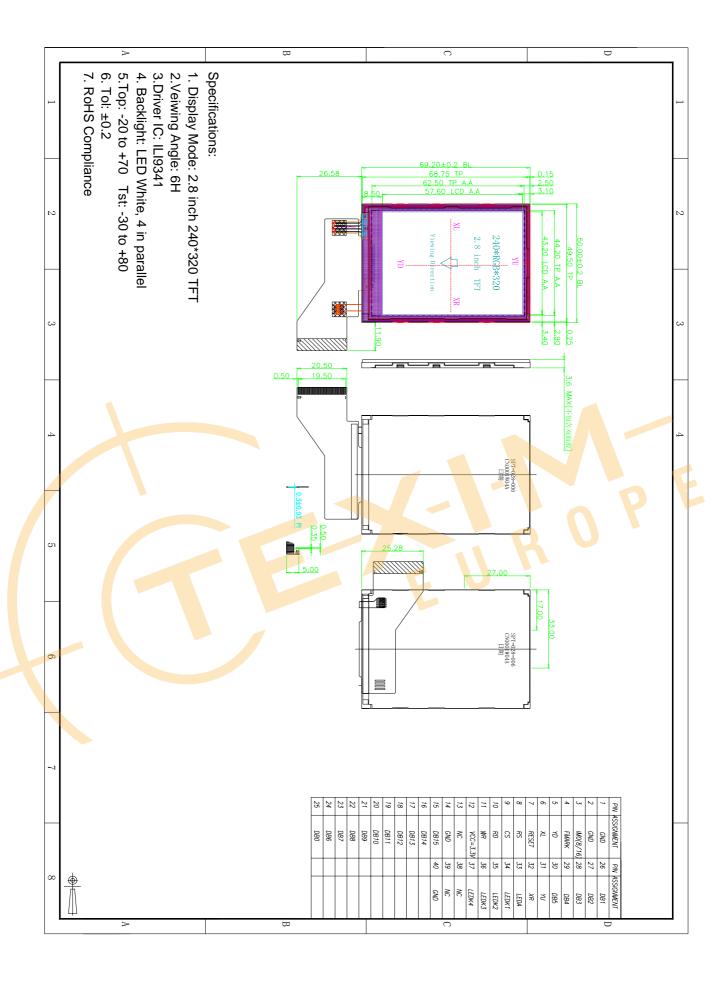
Licetifical 7 Optical Grialacteristics								
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage	VF	.=	2.8	3.2	3.5	V		
Average Brightness (Without LCD)	IV	IF=60mA VF=3.2 V	3500		A	cd/m ²		
Color of CIE Coordinate	X	4 white leds Ta =25°C	0.26	-	0.31	1		
(without LCD)	Y	13 23	0.26		0.31	P -		
Color			White	B	U			

2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram







2.2 Interface Pin Description

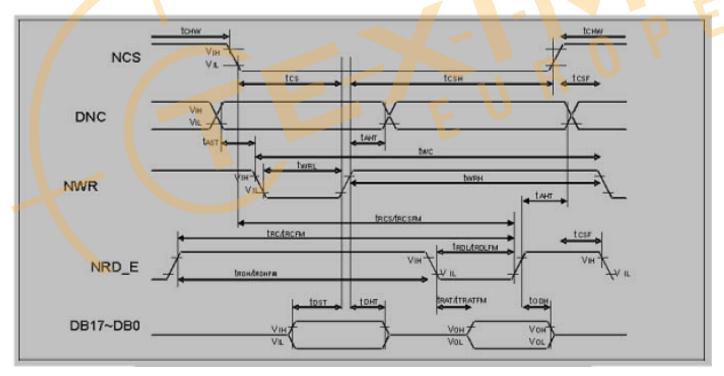
Pin No.	Symbol	Function					
1	GND	System Ground.(0V)					
2	GND	System Ground.(0V)					
3	IM0(8/16)	Interface Mode					
4	FMARK	Output a frame head pulse signal.					
5	YD	TOUCH PANEL SIGNAL OUTPUT PIN					
6	XL	TOUCH PANEL SIGNAL OUTPUT PIN					
7	RESET	RESET SIGNAL					
8	RS	Register selecte siganl					
9	CS	Chip selecte signal					
10	RD	Read signal					
11	WR	Write signal					
12	VCC=3.3V	Analog power supply.					
13	NC	NO Connection					
14	GND	System Ground.(0V)					
15~22	DB15~DB8	Data Bus Bit					
23	DB7	Data Bus Bit					
24	DB6	Data Bus Bit					
25	DB0	Data Bus Bit					
26	DB1	Data Bus Bit					
27	DB2	Data Bus Bit					
28	DB3	Data Bus Bit					
29	DB4	Data Bus Bit					
30	DB5	Data Bus Bit					
31	YU	TOUCH PANEL SIGNAL OUTPUT PIN					
32	XR	TOUCH PANEL SIGNAL OUTPUT PIN					
33	LEDA	POWER SUPPLYFOR LED BACKLIGHT+					
34	LEDK1	POWER SUPPLYFOR LED BACKLIGHT-					
35	LEDK2	POWER SUPPLYFOR LED BACKLIGHT-					



36	LEDK3	POWER	SUPPLYFOR	LED	BACKLIGHT-				
37	LEDK4	POWER	SUPPLYFOR	LED	BACKLIGHT-				
38	NC	NO Conn	NO Connection						
39	NC	NO Conn	NO Connection						
40	GND	System (System Ground.(0V)						

2.3 Timing Characteristics

2.3.1 Parallel 8080 Timing Characteristics





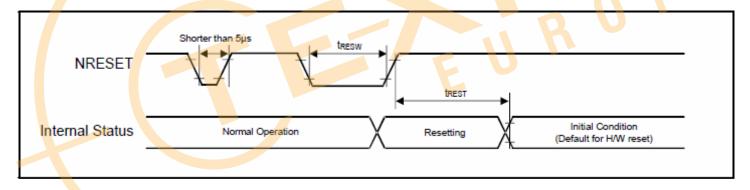
(VSSA=0V, IOVCC=1.65V to 3.6V, VCI=2.5V to 3.6V, Ta = -30 to 70℃)

Signal	Symbol	Parameter	Min.	Max.	Unit	Description
DNC	tast	Address setup time	0	-	ns	
DINC	taht	Address hold time (Write/Read)	_ 10	-	115	-
	tchw	Chip select "H" pulse width	ン 0	-		
	tcs	Chip select setup time (Write)	15	-		
NCS	trcs	Chip select setup time (Read ID)	45	-	ns	_
NOS	trosem	Chip select setup time (Read FM)	355	-	113	_
	tcsf	Chip select wait time (Write/Read)	10	-		
	tcsn (Chip select hold time	10	-		
	twc	Write cycle	66	-		
NWR_RNW	twrn	Control pulse "H" duration	15	-	ns	-
	twrL	Control pulse "L" duration	15	-		
~ ^ ^	trc	Read cycle (ID)	160	-		
NRD_E (ID)	tron	Control pulse "H" duration (ID)	90	-	ns	When read ID data
^ /^	trol	Control pulse "L" duration (ID)	45	-		
	* trcfm	Read cycle (FM)	450	-		When read from frame
NRD_E (FM)	trdhfm	Control pulse "H" duration (FM)	90	-	ns	
	trdlfm	Control pulse "L" duration (FM)	355	-		memory
	tost	Data setup time	10	-		
	toht	Data hold time	10	-		For maximum CL=30pF
D15 to D0	trat	Read access time (ID)	-	40	ns	For minimum CL=8pF
	tratem	Read access time (FM)	-	340		1 of Hillimum CL-ope
	toрн	Output disable time	20	80		

Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.

Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

2.3.2 Reset Timing Characteristics

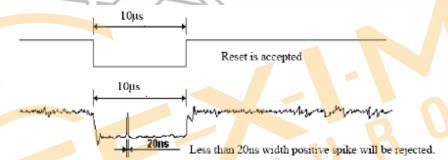


Symbol	Parameter	Related Pins	Min.	Тур.	Max.	Note	Unit
tresw	Reset low pulse width ⁽¹⁾	NRESET	10	-	-	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	μs
trest	Reset complete time ⁽²⁾	-	-	1	5	When reset applied during Sleep in mode	ms
(REST	Reset complete time	-		1	120	When reset applied during Sleep Out mode	ms

Note: (1) Spike due to an electrostatic discharge on !RES line does not cause irregular system reset according to the following table.

NRESET Pulse	Action		
Shorter than 5µs	Reset Rejected		
Longer than 10µs	Reset		
Between 5µs and 10µs	Reset Start		

- (2) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep out –mode. The display remains the blank state in Sleep In –mode) and then return to Default condition for H/W reset.
- (3) During Reset Complete Time, ID2 and VCOMOF value in OTP will be latched to internal register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of RESET.
- (4) Spike Rejection also applies during a valid reset pulse as shown as below:

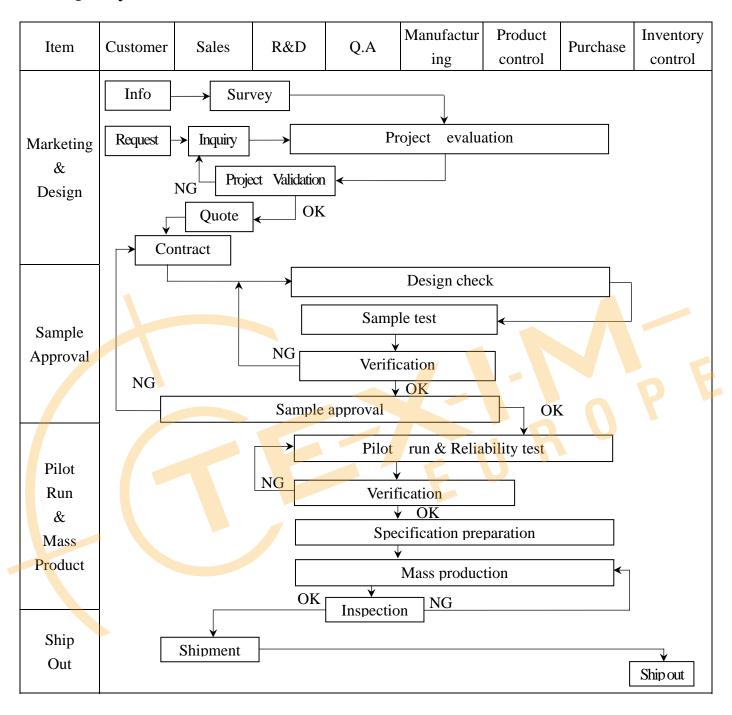


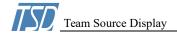
(5) It is necessary to wait 5msec after releasing RESET before sending commands. Also Sleep Out command cannot be sent for 120ms.

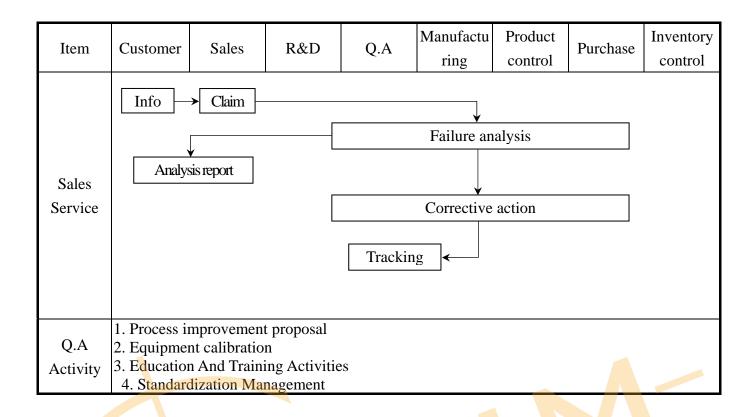


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for less than 3, 5" (Ver.02).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge ` MIL-STD ` Powertip Tester ` Sample

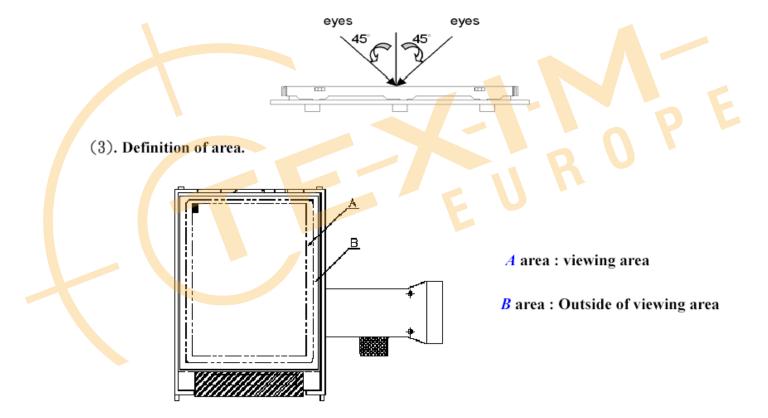
◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

♦OUT Going Defect Level: Sampling.

◆Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(4). Standard of inspection: (Unit: mm)

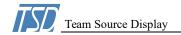


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NO	Item		Criteri	on	Level			
		1. 1The part nu production		t with work order of	Major			
01	Product condition	1, 2 Mixed proc	luct types.		Major			
		1. 3 Assembled	in inverse direction.		Major			
02	Quantity	2. 1The quantit	. 1The quantity is inconsistent with work order of production.					
03	Outline dimension	3. 1 Product di diagram.	re Major					
		4. 1 Missing line character and icon.						
		4. 2 No function	4. 2 No function or no display.					
04	Electrical Testing	4. 3 Display malfunction.						
		4. 4 LCD viewing angle defect.						
		4.5 Current consumption exceeds product specifications.						
			Item	Acceptance (Q'ty)				
	Dat defeat		Bright Dot	≦ 2				
	Dot defect	Dot	Dark Dot	≦ 3				
	(Bright dot \	Defect	Joint Dot	≦ 2				
05	Dark dot)		Total	≦ 3	Minor			
	On -display	5. 1 Inspection pattern: full white, full black, Red, Green and						
			blue screens.					
			d as dot defect if def					
		5, 3 The distance	ce between two dot o	lefect ≥5 mm.				

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NO	Item	Cr	riterion	Level
	Black or white dot \ scratch \ contamination	6. 1 Round type (Non-displated Dimension (diameter : Φ $\Phi \leq 0.15$ $0.15 < \Phi \leq 0.20$ $0.20 < \Phi \leq 0.30$		
06	Round type $ \begin{array}{c c} X & & \downarrow \\ \hline Y & & \downarrow \\ \hline & & \downarrow \\ \hline$	Φ > 0.30 Total 6. 2 Line type(Non-display of the state of the stat	3	Minor
	Line type Line type L W	$L \le 5.0$ $0.03 < W$	/ ≤ 0.03 Ignore	
07	Polarizer Bubble	Dimension (diameter : Φ) $\Phi \le 0.20$ $0.20 < \Phi \le 0.50$ $\Phi > 0.50$ Total	Acceptance (Q'ty) Ignore 3 0	Minor



(Ver. 02)

	Z: The th t: The thi	ickness of crack	Y: The width of crack. W: terminal length a: LCD side length ack between panels:	
The crack of glass	SP-	Y [OK]	[NG]	Minor
	X	Y	z	
	≤ a	Crack can't enter viewing area	≤1/2 t	
	≦ a	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	
	The crack of glass	X ≤ a	Seal width X Y $\leq a$ Crack can't enter viewing area $\leq a$ Crack can't exceed the	The crack of glass Y [NG] X Y Seal width X Y \leq a Crack can't enter viewing area \leq 1/2 t \leq a Crack can't exceed the 1/2 t \leq 7 \leq 2 t



(Ver. 02)

NO	Item	Criterion					
		Z : The thi	ngth of crack ickness of crack ickness of glass ner crack :	W : teri	e width of crack. minal length O side length		
		X	Y		Z		
		≦1/5 a	Crack can't e viewing are	1 7	$L \leq 1/2 t$		
		≦1/5 a	Crack can't exc half of SP wi	1/7	t < Z ≤ 2 t		
08	The crack of glass		sion over terming on electrode		Y Z	Mino	
			X	Y	Z		
		Front Back		$\leq 1/2 \text{ W}$ $\leq \text{W}$	≤ t ≤ 1/2 t		
		Dack					



(Ver. 02)

NO	Item	Criterion		
		Criterion Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 2. 2 Non-conductive portion:	Level	
08	The crack of glass	Y X X Y Z ≤ 1/3 a ≤ W ≤ t O If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8. 2. 3 Glass remain:	Minor	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$		



(Ver. 02)

Speci	cification For TFT-LCD Module Less Than 3, 5":				
NO	Item	Criterion	Level		
09	Backlight elements	9. 1 Backlight can't work normally.	Major		
		9. 2 Backlight doesn't light or color is wrong.	Major		
		9. 3 Illumination source flickers when lit.	Major		
	General appearance	10. 1 Pin type \quantity \dimension must match type in structure diagram.	Major		
		10, 2 No short circuits in components on PCB or FPC.	Major		
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart . There should be no wrong parts, missing parts or excess parts.	Major		
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor		
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor		
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is $~\leq 1, 5$ mm.	Minor		

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4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION			
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.		hrs.	
2	Low Temperature Storage Test	Keep in -30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60°C /90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer & T/P)			
		Air Dischar	ge:	Contact Discharge:	
		(include mo	bile phone)	(include mobile phone)	
		Apply 2 KV	with 5 times	Apply 250V with 5 times	
		Discharge fo	or each polarity +/-	discharge for each polarit	y +/-
4	ESD Test	 Temperature ambiance:15°C ~35°C Humidity relative:30% ~60% Energy Storage Capacitance(Cs+Cd):150pF±10% Discharge Resistance(Rd):330Ω±10% Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 s) 			
5	Temperature Cycling Storage Test	(Tolerance if the output voltage indication: $\pm 5\%$) -20°C \rightarrow +25°C \rightarrow +70°C \rightarrow +25°C (30mins) (30mins) (30mins) (30mins) 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 			
			Packing Weight (Kg)	Drop Height (cm)	
7			0 ~ 45.4	122	
	Drop Test		45.4 ~ 90.8	76	
	(Packaged)		90.8 ~ 454	61	
			Over 454	46	
		Drop direction: * 1 corner / 3 edges / 6 sides each 1 times			

5. PRECAUTION RELATING PRODUCT HANDLING



5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

Packing

PARAMETER	Specification	Unit
Outside box	390(L) x 350(W) x 480(H)	mm
Inside pearl wool box	330(L)x185(W)x110(H)	mm
Product quantity of Inside box	64	pcs
Total product quantity	64*8=512	pcs
Total weight	13.5 ± 0.5	Kg



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Please contact us if you have any questions about the contents of the datasheet.

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