

# **TFT Module Specification**

# MODEL: 13-070WMTB00A1-S

< 🔷 >	PRELIMINARY SPECIFICATION
< •>	APPROVAL SPECIFICATION

CUSTOMER
APPROVED BY
DATE:

DESIGNED	CHECKED	APPROVED
RD 2013.12.09 Kobe		PM 2013.12.09 呂家祥

# **RECORD OF REVISION**

Version	Revised Date	Page	Content
V1.0	2013/11/27		First Issued

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#### 1. GENERAL DESCRIPTION

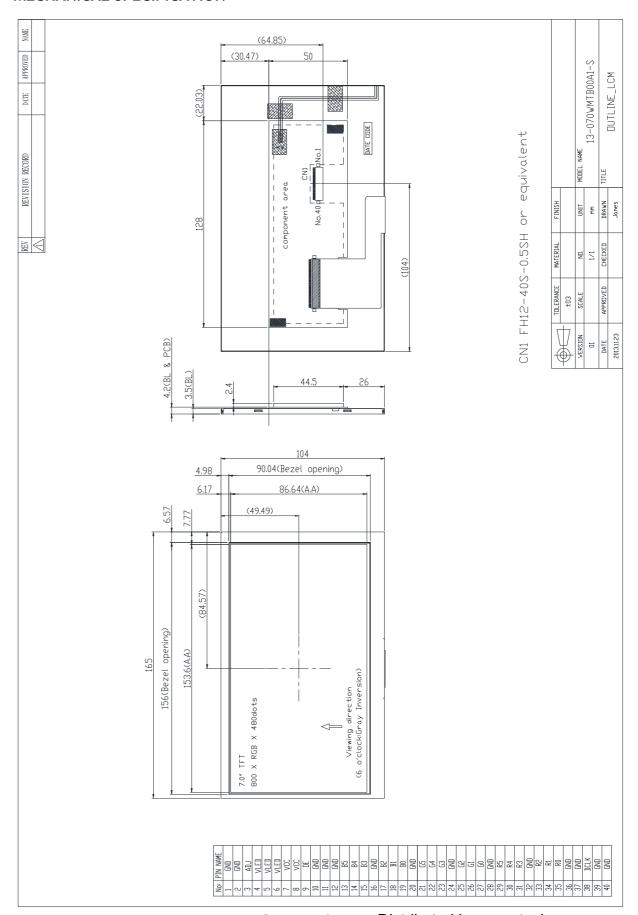
#### 1.1 Description

The specifications is model 13-070WMTB00A1-S is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a back light system. This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with WVGA (800 horizontal by 480 vertical pixels) resolution.

#### 1.2 Features:

No.	Item	Specification	Unit
1	Panel Size	7.0"	Inch
2	Number of Pixels	800 (W) x RGB x 480 (H)	Pixels
3	Active Area	153.6 (W) × 86.64 (H)	mm
4	Pixel Pitch	0.192 (W) x 0.1805 (H)	mm
5	Outline Dimension	165 (W) × 104 (H) × 3.5 (T)	mm
6	Number of Colors	262K	
7	Display Mode	TN / Normally White / Transmissive	
8	View Direction	6 o'clock(Gray Inversion)	
9	Display Format	RGB vertical stripe	
10	Surface Treatment	Anti-Glare	
11	Contrast Ratio	500 (Typ.)	
12	Luminance (cd/m^2)	1000 (Typ.)	cd/m2
13	Interface	RGB 18bit Interface	
14	Backlight	White LED	
15	Operation Temperature	-20 ~ 70	°C
16	Storage Temperature	-30 ~ 80	°C
17	Weight	(TBD)	g

# 2. MECHANICAL SPECIFICATION



# 3. PIN DESCRIPTION

# 3.1 TFT LCD Module

I LCD MOC	1410		
Symbol	1/0	Function	Remark
GND	Р	Ground	
GND	Р	Ground	
ADJ		Brightness control for LED B/L	Note 3
VLED	Р	Power Supply for LED Driver	
VLED	Р	Power Supply for LED Driver	
VLED	Р	Power Supply for LED Driver	
VCC	Р	Power Supply for system	
VCC	Р	Power Supply for system	
DE		Data Enable Timing Signal	
GND	Р	Ground	
GND	Р	Ground	
GND	Р	Ground	
B5	I	Blue data signal (MSB)	
B4	I	Blue data signal	
В3	I	Blue data signal	
GND	Р	Ground	
B2	I	Blue data signal	
B1	I	Blue data signal	
В0	ı	Blue data signal (LSB)	
GND	Р	Ground	
G5	I	Green data signal (MSB)	
G4	I	Green data signal	
G3	I	Green data signal	
GND	Р	Ground	
G2	I	Green data signal	
G1	I	Green data signal	
G0	I	Green data signal (LSB)	
GND	Р	Ground	
R5	I	Red data signal (MSB)	
R4	I	Red data signal	
R3	I	Red data signal	
GND	Р	Ground	
R2		Red data signal	
	Symbol GND GND VLED VLED VLED VCC VCC DE GND GND GND B5 B4 B3 GND B2 B1 B0 GND GND GSD GND GSD GND GND GSD GND GND GSD GND GND GSD GND GSD GND GND GSS GA GND GSS GA GND GSS GA GND GSS GND GSS GA GND GSS GND GND RSS R4 R3 GND	GND         P           GND         P           ADJ         I           VLED         P           VLED         P           VCC         P           VCC         P           DE         I           GND         P           GND         P           B5         I           B4         I           B3         I           GND         P           B2         I           B1         I           B0         I           GND         P           G5         I           G4         I           G3         I           GND         P           G2         I           G1         I           GND         P           R5         I           R4         I           R3         I           GND         P	Symbol I/O Function GND P Ground GND P Ground ADJ I Brightness control for LED B/L VLED P Power Supply for LED Driver VCC P Power Supply for system VCC P Power Supply for system DE I Data Enable Timing Signal GND P Ground GND P Ground B5 I Blue data signal (MSB) B4 I Blue data signal B3 I Blue data signal B1 Blue data signal B2 I Blue data signal B1 I Blue data signal B1 I Blue data signal B2 I Green data signal B3 I Green data signal CND P Ground CS I Green data signal CND P Ground

34	R1	I	Red data signal	
35	R0	I	Red data signal (LSB)	
36	GND	Р	Ground	
37	GND	Р	Ground	
38	DCLK	ı	Data Clock	
39	GND	Р	Ground	
40	GND	Р	Ground	

# 4. ABSOLUTE MAXIMUM RATINGS

# 4.1 Electrical Absolute Rating

## 4.1.1 TFT LCD Module

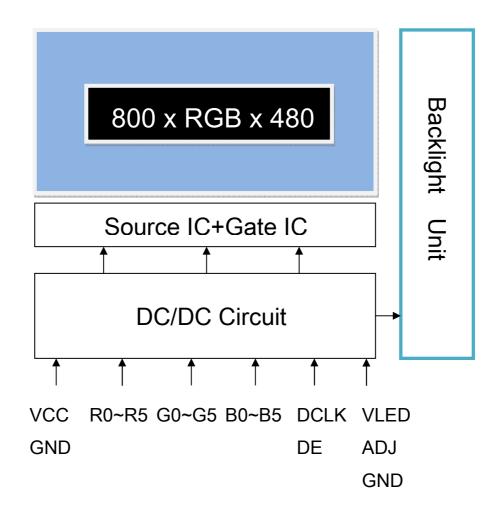
ltem	Symbol	Val	ues	Unit	Note
iteiii	Symbol	Min	Max.	Offic	Note
Power supply voltage	VCC	-0.3	4.0	٧	
Power supply vollage	VLED	0	6.0	٧	

# 4.1.2 Environment Absolute Rating

ltem	Symbol		Values	Unit	Note		
iteiii	Symbol	Min	Тур	Max.	Oili	Note	
Operating Temperature	Topa	-20		70	°C	Ambient	
Storage Temperature	Tstg	-30		80	°C	temperature	

#### 5. BLOCK DIAGRAM

#### 5.1 TFT LCD Module



12/9/2013

# 6. Relationship Between Displayed Color and Input

#### 6.1 6 bit

Color & Gray									D	ata S	Signa	l							
	Scale	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	В0
	Black						0	0	0	0	0	0	0	0	0				
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Oreen	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
لبيا	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage, 1 :High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. With the combination of total 18 bit data signals, the 262K-color display can be achieved on the screen.

#### 7. ELECTRICAL CHARACTERISTICS

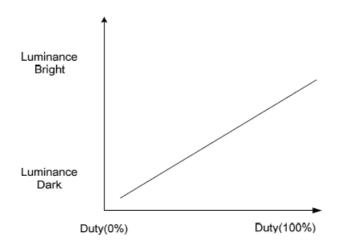
#### 7.1 TFT LCD Module

Item		Symbol	Symbol Value			Unit	Note		
		Symbol	Min.	Тур.	Max.	Offic	Note		
Power supply	voltago	VCC	3.0	3.3	3.6	٧			
Power supply	vollage	VLED	4.5	5	5.5	٧			
Input	H Level	VIH	0.7xVCC	-	VCC	٧			
Voltage for logic	L Level	VIL	0	-	0.3xVCC	٧			
PWM frequenc	СУ	ADJ	19K	20K	21K	Hz	Note2		
Digital Curron	Di ii I C		ICC		-	(120)	(150)	mA	Note1
Digital Current		ILED	-	(450)	(550)	mA			
LED Life Time	(25°℃)	-	(30000)	-	-	hr	Note3		

Note 1: frame =60Hz , Ta=25°C , Display pattern : Black pattern



Note 2: ADJ signal is 0~3.3V.Operation frequency is 20KHz

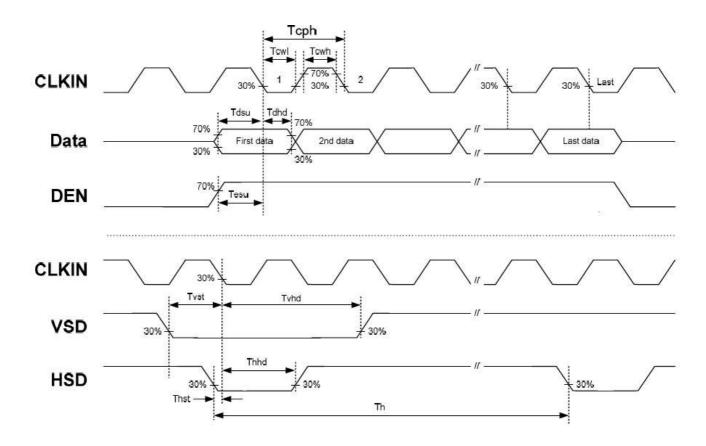


Note 3: The "LED life time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is  $25^{\circ}$ C 60% RH.

#### 7.2 INTERFACE SPECIFICATIONS

# 7.2.1 AC Timing characteristics

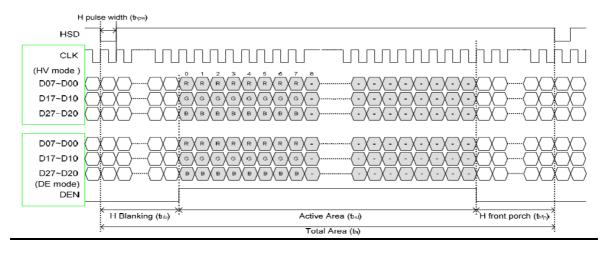
Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
HSYNC	HS setup time	Thst	8	-	-	ns	
HISTING	HS hold time	Thhd	8	-	-	ns	
VSYNC	VS setup time	Tvst	8	-	-	ns	
	VS hold time	Tvhd	8	-	-	ns	
Data	Data setup time	Tdsu	8	-	-	ns	
	Data hold time	Tdhd	8	-	-	ns	
DE	DEN setup time	Tvpw	8	-	-	ns	
	DEN hold time	Tvb	8	-	-	ns	



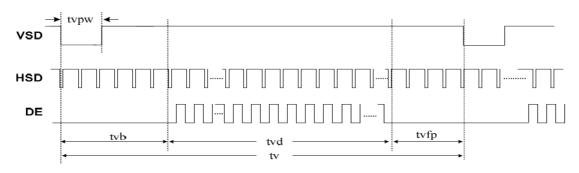
7.2.2 DE Mode Input Timing Table

Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
	CLK frequency	Fclk	-	30	50	MHz	
DCLK	CLK period	Tcph	20	-	-	ns	
	CLK pulse duty	Tcwh	40	50	60	%	
	Horizontal Line	Th	862	1056	1200	CLK	
	HS Display Area	Thd	-	800	-	CLK	
HSYNC	HS Pulse Width	Thpw	1	-	40	CLK	
	HS Back Porch	Thb	-	46	-	CLK	
	HS Front Porch	Thfp	16	210	354	CLK	
DE DE Mode Blanking		Th-Thd	85	256	400	CLK	
VSYNC	VS Display Area	Tvd	-	480	-	th	
	VS Period Time	Tv	513	525	650	th	
	VS Pulse Width	Tvpw	3	-	20	th	
	VS Back Porch	Tvb	-	23	-	th	
	VS Front Porch	Tvfp	1	12	77	th	
DE	DE DE Mode Blanking Tv-Tvd		30	45	170	th	

#### Horizontal input timing

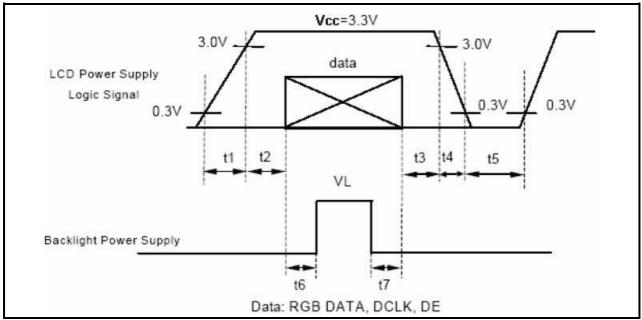


#### Vertical input timing



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# 7.3 Power On / Off Sequence



 $t1 \le 10ms : 1 sec \le t5$  $50ms \le t2 : 200ms \le t6$ 

0<t3 ≤50ms: 200ms≤ t7

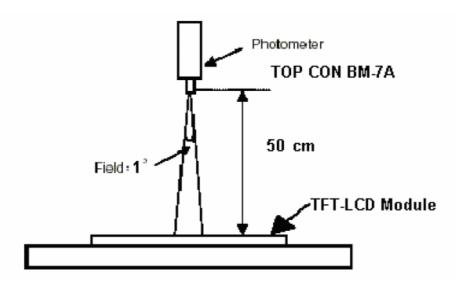
0<t4 ≤10ms

#### 8. OPTICAL CHARACTERISTICS

lter	n	Symbol	Condition	Min.	Тур.	Max.	Unit
Bright	ness			800	1000		cd/m2
Uniformity		B-uni	Note1,	70	75	-	%
Contrast Ratio		CR	Note 3,	400	500		
Dannana Tima		Tr	$(\theta = 0^\circ,$ Normal		10		ms
Response	Response Time		Viewing		15		ms
Color	White	Wx	Angle)	0.260	0.310	0.360	
Chromaticity	wille	Wy		0.280	0.330	0.380	
	Horizontal	<i>θ</i> <b>x</b> +		50	60		
Viou anglo		<i>θ</i> x-	Center	50	60		
View angle	Vertical	θ <b>Y</b> +	CR≥10	40	50		
		θ <b>Y</b> -		50	60		
Image sticking		tis	2 hours			2	Sec

Note: The following optical specifications shall be measured in a darkroom or equivalent state(ambient luminance  $\le 1$  lux, and at room temperature). The operation temperature is  $25^{\circ}C\pm2^{\circ}C$ . The measurement method is shown in Note1.

Note1: The method of optical measurement:

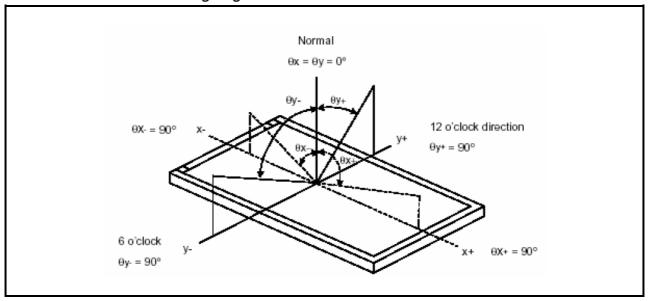


Note2: Measured at the center area of the panel and at the viewing angle of the  $\theta x = \theta y$ =0°

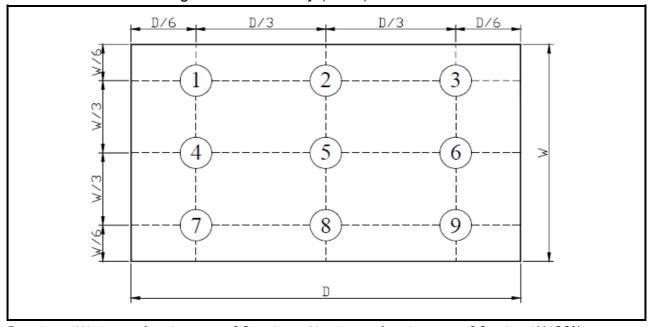
Note3: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state ÷ Luminance with all pixels in Black state

Note4: Definition of Viewing Angle:



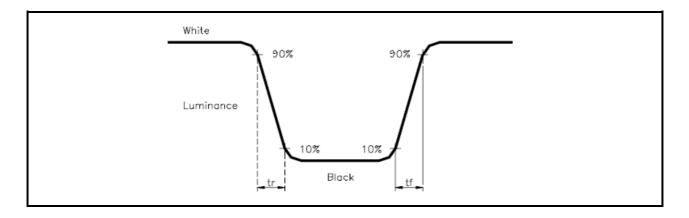
Note 5: Definition of Brightness Uniformity (B-uni):



B-uni = (Minimum luminance of 9 points÷Maximum luminance of 9points)X100%

#### Note 6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time (Tr)" and the "Falling Time (Tf)" respectively. Tr and Tf are defined as following figure



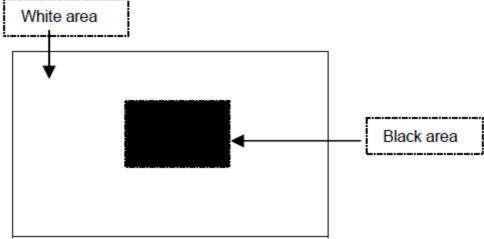
#### Note 7: Definition of Chromaticity:

The color coordinates (Wx,Wy),(Rx,Ry),(Gx,Gy),and (Bx,By) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.

#### Note 8: Definition of Image sticking (tis):

Continuously display the test pattern shown in the figure below for 2 hours. Then display a completely white screen. The previous image shall not persist more than 2 sec at 25 °C

# Image sticking pattern



#### 9. RELIABILITY

#### 9.1 Test Condition

**9.1.1** Temperature and Humidity(Ambient Temperature)

Temperature :  $25 \pm 5^{\circ}C$ Humidity :  $65 \pm 5\%$ 

#### **9.1.2** Operation

Unless specified otherwise, test will be conducted under function state.

#### 9.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

#### **9.1.4** Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

#### **9.2 TESTS**

No.	ITEM	CONDITION CRITERION				
1	High Temperature Storage	80°C, 120 hrs				
2	Low Temperature Storage	-30°C, 120 hrs				
3	High Temperature Operating	70°C, 120 hrs				
4	Low Temperature Operating	-20°C, 120 hrs				
5	High Temperature/Humidity Non-Operating	60°C, 90%RH, 120 hrs				
6	Temperature Shock Non-Operating	$-30^{\circ}\text{C} \longleftrightarrow 80^{\circ}\text{C}$ (0.5hr each), 25 cycles				
7	Vibration Test Non-Operating	Frequency:0 ~ 55 Hz Amplitude:1.5 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z				
8	Electro-static Discharge Non-Operating	150pF,330Ω Air:± 8KV;Contact: ±4KV 10 times/point;4 points/panel face				

Note1: The test sample have recovery time for 24 hours at room temperature before the function check. In the standard conditions, there is no any touch panel function NG issue occurred.

#### 9.3 JUDGMENT STANDARD

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

# 9.4 INCOMING INSPECTION STANDARDS

No.	Parameter	Criteria							
		Display function: No Display malfunction (Major)							
		Contrast ratio (Black, White):  Does not meet specified range in the spec. (Major) (Note:3)							\ \\
		Line D	ofact: No	specifi	ed rar	nge in	tne spec. (	iviajor) (i	Note:3)
		Line Defect: No obvious Vertical and Horizontal line defect in bright							
		dark and colored. (Major) (Note:1)  Point Defect : Active area ≤ 5 dots (Minor) (Note:1)							
							number		
			Iter	n		Active		Total	
			Brig	ht		2	,		
			Dar			4		5	
			Dui	K					
1	Onevetina								
' '	Operating	Nonu	niformity:	\/ieibl/	o throi	uah 5º	%ND filter.	(Minor)	
							e spots sha		1/41 )
		. 01619	- materia				Clas		
				Zone		ptable	Of		AQL
			Dimensi	on	nur	mber	Defec	ts	Level
			D> 0	_		0			
			0.3 < D			5	Mino	r	1.5
			D ≤ 0			*	7		
			D = (Lon	g + Sh	ort) /	2 *	: Disregard	1	
		Foreig	gn Materi			spiral	shape (W≤	1/4L) (N	lote: 4)
					Zone	Δ	cceptable	Class	AQL
				١٨//	,	.	number	Of	Lovel
		L (mi	m) <u> </u>	W(mn	<u>n)</u> √>0.1		0	Defects	5
			<u> </u>		<u>v&gt;0.1</u> < W⊴	0.1	5	Minor	1.5
			. ≤0.5		<u>&lt;0.03</u>		*	IVIIIIOI	1.0
		_ —	Length		Width		Disregard		
			nsion: Οι				21010gu.u.		
		Bezel	appeara	nce: u	never	(Mino			
		Scrate	ch on the						
				$\setminus$ Z	one A	Accept	a Clas		AQL
			(	W(mm		ble	Of Det	ects	Level
		<u> </u>	(mm)	W>0		numbe 0	Min		1.5
		-	 L ≤ 3			3	IVIIII	OI	1.5
			LSJ	VV≥U	. 1				
	External Inspection	1	: Length	w·	Width	* • [	Disregard		
2	(non-operating)		r bubble (						
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		Zon				Class	AQL	
						eptable mber	Of	Leve	I
			Dimensio	_	iidi		Defects		
			D≤0.:			*	Minor	1.5	
		L	D≤0.	5		3			
		ח	= (Long ·	+ Shor	t) / 2		* : Disi	enard	
			Long	. 01101	912		٠. اداما	ogara	

			Definition			
Class of	Major	AQL 0.65%	It is a defect that is likely to result in failure or to reduce materially the			
defects			usability of the product for the intended function.			
defects	Minon	AQL 1.5%	It is a defect that will not result in functioning problem with deviation			
	Minor	AQL 1.5%	classified.			

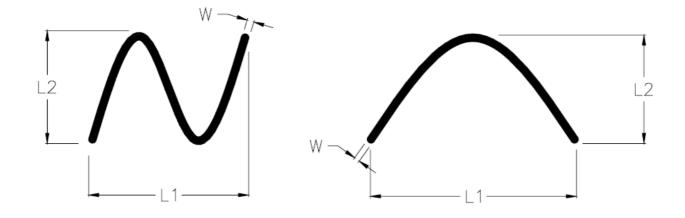
#### Note1:

- (a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively (b)Dark point defect is defined as visible in full white pattern.
- (c)Definition of distribution of point defect is as follows:
  - -minimum separation between dark point defects should be larger than 5mm.
  - -minimum separation between bright point defects should be larger than 5mm.
- (d)Definition of joined bright point defect and joined dark point defect are as follows:
  - -Two or more joined bright point defects must be nil.
  - -Three joined dark point defects must be nil.
  - -Coupling of one dark and one bright point in junction is counted as one dark and bright spot with 1 pair maximum.
  - -Two Joined dark point is counted as two dark points with 2 pair maximum.

Note2: The external inspection should be conducted at the distance  $30\pm$  5cm between the eyes of inspector and the panel.

Note3: Luminance measurement for contrast ratio is at the distance  $50\pm$  5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note4: W-Width in mm, L-length of Max.(L1,L2) in mm.



### 9.5 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

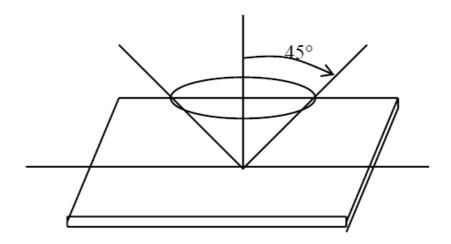
Sampling table: MIL-STD-105E Inspection level: Level II

### 9.6 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.

 $\theta \le 45^{\circ}$  inspection under non-operating condition.

 $\theta \le 5^{\circ}$  inspection under operating condition



#### 10. PRECAUTION RELATING PRODUCT HANDLING

#### **10.1 SAFETY**

- 10.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 10.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 10.2 HANDLING

- 10.2.1 Avoid any strong mechanical shock which can break the glass.
- 10.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 10.2.3 Do not remove the panel or frame from the module.
- 10.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, Do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 10.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 10.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 10.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 10.2.8 To control temperature and time of soldering is 280 ± 10°C and 3-5 sec.
- 10.2.9 To avoid liquid (include organic solvent) stained on LCM.

#### 10.3 STORAGE

- 10.3.1 Store the panel or module in a dark place where the temperature is  $25 \,^{\circ}$ C ±  $5 \,^{\circ}$ C and the humidity is below 65% RH.
- 10.3.2 Do not place the module near organics solvents or corrosive gases.
- 10.3.3 Do not crush, shake, or jolt the module.



# **Contact details**

#### **The Netherlands**



Elektrostraat 17 NL-7483 PG Haaksbergen

T: +31 (0)53 573 33 33 F: +31 (0)53 573 33 30 E: nl@texim-europe.com

# **Belgium**



Zuiderlaan 14 bus 10 B-1731 Zellik

T: +32 (0)2 462 01 00 F: +32 (0)2 462 01 25

E: belgium@texim-europe.com

#### **UK & Ireland**







St. Mary's House, Church Lane Carlton Le Moorland Lincoln LN5 9HS

T: +44 (0)1522 789 555 F: +44 (0)845 299 22 26 E: uk@texim-europe.com

# **Germany North**



Bahnhofstrasse 92 D-25451 Quickborn

T: +49 (0)4106 627 07-0 F: +49 (0)4106 627 07-20 E: germany@texim-europe.com

# **Germany South**



Martin-Kollar-Strasse 9 D-81829 München

T: +49 (0)89 436 086-0 F: +49 (0)89 436 086-19

E: germany@texim-europe.com

#### Austria



Warwitzstrasse 9 A-5020 Salzburg

T: +43 (0)662 216 026 +43 (0)662 216 026-66 E: austria@texim-europe.com

# **Nordic region**



Sdr. Jagtvej 12 DK-2970 Hørsholm

T: +45 88 20 26 30 F: +45 88 20 26 39

E: nordic@texim-europe.com

#### Italy



Via Matteotti 43 IT-20864 Agrate Brianza (MB)

T: +39 (0)39 971 3293 F: +39 (0)39 971 3293 E: italy@texim-europe.com

## General information



info@texim-europe.com www.texim-europe.com





