WINSTAR Display

OLED SPECIFICATION

Model No:

WEO012832K

General Specification

Item	Dimension	Unit
Dot Matrix	128 x 32	Dots
Module dimension	59.0 x 18.86 x 1.41	mm
Active Area	50.535 x 10.695	mm
Pixel Size	0.370 x 0.310	mm
Pixel Pitch	0.395 x 0.335	mm
Display Mode	Passive Matrix	
Display Color	Monochrome	
Interface	8Bits 68xx 80xx/ 4-Wire SPI/ I2C	
Drive Duty	1/32 Duty	
IC	SSD1315	
Size	2.0 inch	

Contour Drawing & Block Diagram
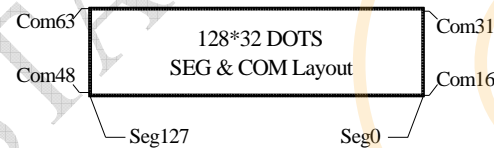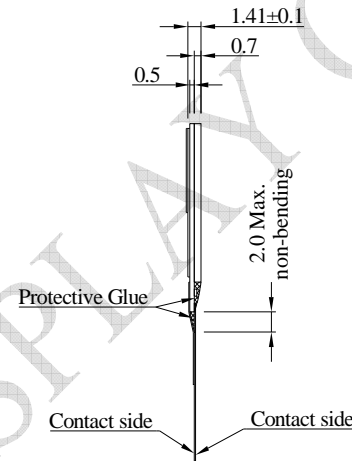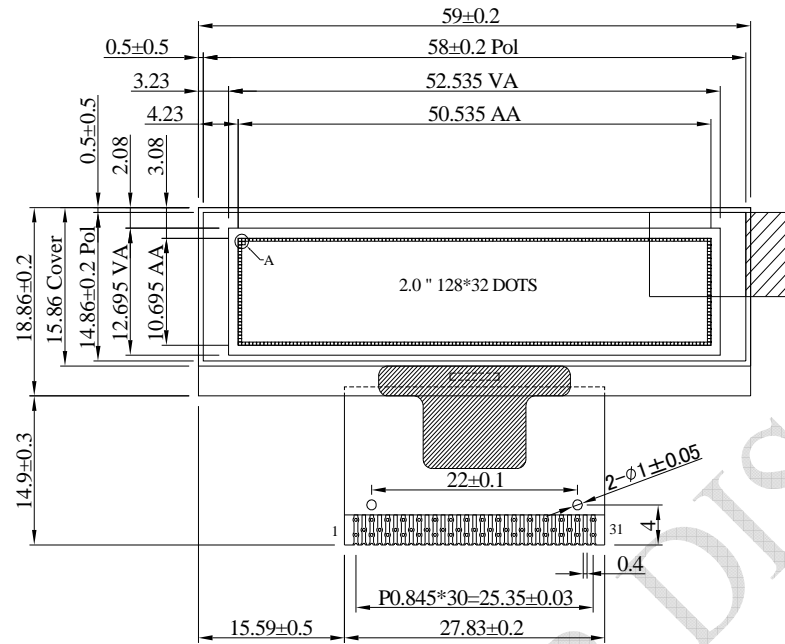
PIN	SYMBOL	PIN	SYMBOL
1	NC	17	CS#
2	VCC	18	NC
3	VCOMH	19	BS2
4	IREF	20	BS1
5	D7	21	VDD
6	D6	22	NC
7	D5	23	NC
8	D4	24	NC
9	D3	25	NC
10	D2	26	NC
11	D1	27	NC
12	D0	28	NC
13	E/RD#	29	NC
14	R/W#	30	VSS
15	D/C#	31	NC
16	RES#		

2.0 " 128*32 DOTS

59±0.2
58±0.2 Pol
52.535 VA
50.535 AA
0.5±0.5
3.23
4.23
0.5±0.5
2.08
3.08
18.86±0.2
15.86 Cover
14.86±0.2 Pol
12.695 VA
10.695 AA
14.9±0.3

1.41±0.1
0.7
0.5
2.0 Max. non-bending

Protective Glue
Contact side
Contact side

22±0.1
2-φ1±0.05
1
31
4
0.4
P0.845*30=25.35±0.03
15.59±0.5
27.83±0.2

DETAIL A
SCALE 20/1

0.395
0.37
0.025
0.335
0.31
0.025

Com63
Com48
128*32 DOTS
SEG & COM Layout
Com31
Com16
Seg127
Seg0
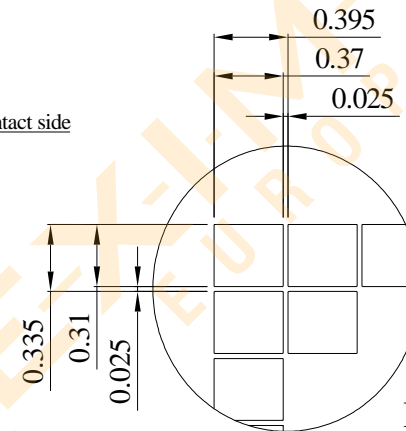
The non-specified tolerance of dimension is ±0.3 mm .

WINSTAR

WEO012832K

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Interface Pin Function

No.	Symbol	Function
1	NC	No connection
2	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. When charge pump is enabled, a capacitor should be connected between this pin and VSS.
3	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.
4.	IREF	This is segment output current reference pin. When external IREF is used, a resistor should be connected between this pin and VSS to maintain the IREF current at 30uA.
5	D7	These are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the serial clock input, SCL.
6	D6	
7	D5	
8	D4	
9	D3	
10	D2	
11	D1	
12	D0	
13	E/RD#	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
14	R/W#	This is read / write control input pin connecting to the MCU interface. When interfacing to a 6800-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH (i.e. connect to VDD) and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
15	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
16	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin HIGH (i.e. connect to VDD) during normal operation.
17	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).

18	NC	No connection
19	BS2	MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2, BS1 are pin select
20	BS1	<table><tr><td>BS[2:1]</td><td>Interface</td></tr><tr><td>00</td><td>4 line SPI</td></tr><tr><td>01</td><td>I2C</td></tr><tr><td>11</td><td>8-bit 8080 parallel</td></tr><tr><td>10</td><td>8-bit 6800 parallel</td></tr></table> Note (1) 0 is connected to VSS (2) 1 is connected to VDD
21	VDD	Power supply pin for core logic operation. This is a voltage supply pin. It must be connected to external source.
22	NC	No connection
23	NC	
24	NC	
25	NC	
26	NC	
27	NC	
28	NC	
29	NC	
30	VSS	Ground pin. It must be connected to external ground.
31	NC	No connection

Absolute Maximum Ratings

Parameter	Symbol	Min	Typ.	Max	Unit
Supply Voltage for Logic	VDD	-0.3	-	4	V
Supply Voltage for Display	VCC	0	-	18	V
Operating Temperature	TOP	-40	-	+80	°C
Storage Temperature	TSTG	-40	-	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Unless otherwise specified, VSS = 0V , VDD = 2.8 - 3.3V   (Ta = 25°C)

Items		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	Logic	VDD	2.8	3.0	3.3	V
	Operating	VCC	7.5	8.0	8.5	V
Input Voltage	High Voltage	V_{IH}	$0.8 \times V_{DD}$	-	VDD	V
	Low Voltage	V_{IL}	0	-	$0.2 \times VDD$	V
Output Voltage	High Voltage	V_{OH}	$0.9 \times V_{DD}$	-	VDD	V
	Low Voltage	V_{OL}	0	-	$0.1 \times VDD$	V

Symbol	Parameter	Min.	Typ.	Max.	Unit	Condition
ICC	VCC Supply Current	-	11	22	mA	VDD=3V , VCC=8.0, Display 50% ON

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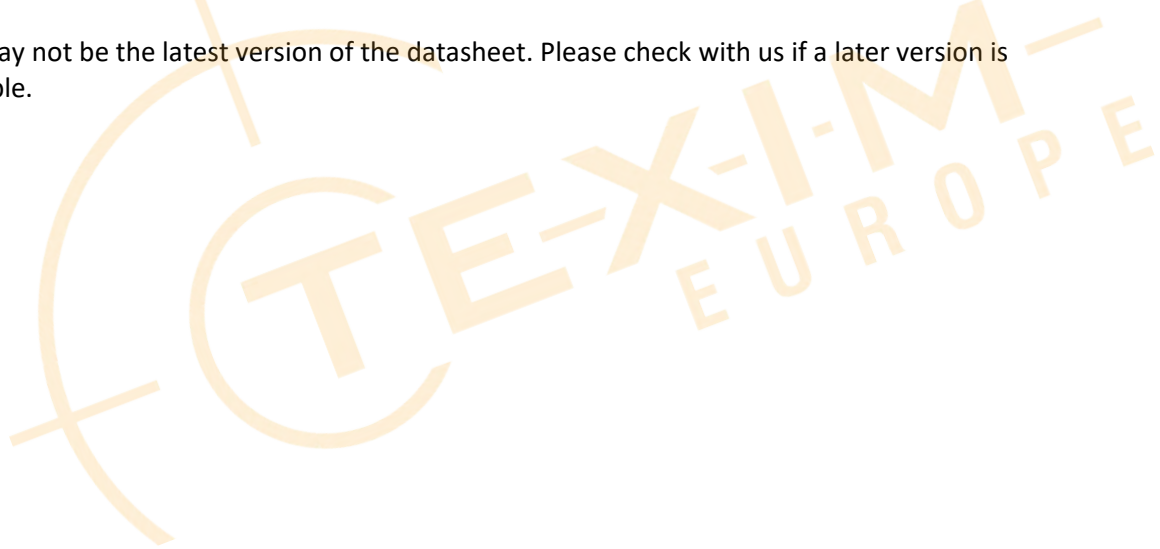
Headquarters & Warehouse

Elektrostraat 17
NL-7483 PG Haaksbergen
The Netherlands

T: +31 (0)53 573 33 33
E: info@texim-europe.com
Homepage: www.texim-europe.com

The Netherlands

Elektrostraat 17
NL-7483 PG Haaksbergen

T: +31 (0)53 573 33 33
E: nl@texim-europe.com

Belgium

Zuiderlaan 14, box 10
B-1731 Zellik

T: +32 (0)2 462 01 00
E: belgium@texim-europe.com

UK & Ireland

St Mary's House, Church Lane
Carlton Le Moorland
Lincoln LN5 9HS

T: +44 (0)1522 789 555
E: uk@texim-europe.com

Germany

Bahnhofstrasse 92
D-25451 Quickborn

T: +49 (0)4106 627 07-0
E: germany@texim-europe.com

Germany

Martin-Kollar-Strasse 9
D-81829 München

T: +49 (0)89 436 086-0
E: muenchen@texim-europe.com

Austria

Warwitzstrasse 9
A-5020 Salzburg

T: +43 (0)662 216 026
E: austria@texim-europe.com

Nordic

Stockholmsgade 45
2100 Copenhagen

T: +45 88 20 26 30
E: nordic@texim-europe.com

Italy

Martin-Kollar-Strasse 9
D-81829 München

T: +49 (0)89 436 086-0
E: italy@texim-europe.com