



Surge arrester

2-electrode arrester

Series/Type: D08-A90SMD Ordering code: T. B. D.

Date: 2016-12-19

Version: 03

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Surge arrester T. B. D.

2-electrode arrester D08-A90SMD

Preliminary data

Features

- Flat design
- High current rating
- Stable performance over life
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications

- Telecom
- Industrial communication
- Line protection
- Subscriber protection
- Alarm system

Electrical specifications

Licotrical specifications		
DC spark-over voltage 1) 2)	90	V
Tolerance	±20	%
Min.	72	V
Max.	108	V
Impulse spark-over voltage		
at 100 V/µs - for 99% of measured values	< 350	V
 typical values of distribution 	< 300	V
at 1 kV/µs - for 99% of measured values	< 550	V
 typical values of distribution 	< 500	V
Service life		
10 operations 50 Hz, 1 s	5	Α
10 operations 8/20 μs	15	kA
1 operation 8/20 μs	20	kA
2 operations 10/350 μs	2.5	kA
300 operations 10/1000 μs	100	А
Insulation resistance at 50 V _{DC}	> 1	GΩ
Capacitance at 1 MHz	< 2	pF
Arc voltage at 1 A	~ 10	V
Glow to arc transition current	< 0.5	Α
Glow voltage	~ 60	V
Weight	~ 0.5	g
Operation and storage temperature	-40 +12 5	°C
Climatic category (IEC 60068-1)	40/125/21	
Marking	without	
•		

¹⁾ At delivery AQL 0.65 level II, DIN ISO 2859

Terms in accordance with ITU-T Rec. K.12 and IEC 61643-311

²⁾ In ionized mode

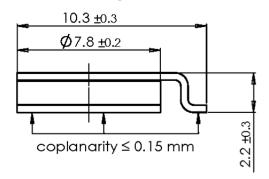


Surge arrester T. B. D.

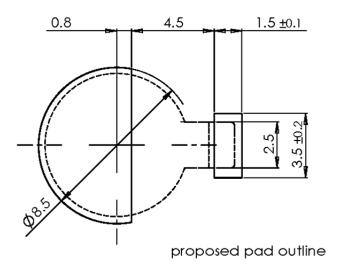
2-electrode arrester D08-A90SMD

Preliminary data

Dimensional drawing in mm







Ordering code and packing advice

T. B. D.



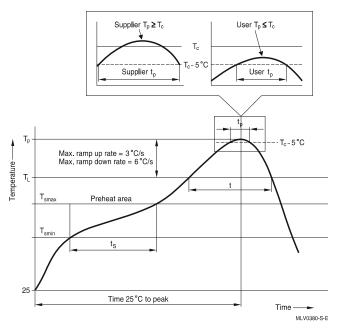
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Preliminary data

Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time Average ramp-up	T_{smin} T_{smax} t_{smin} to t_{smax} T_{smax} to T_{p}	100 °C 150 °C 60 120 s max. 3 °C/s	150 °C 200 °C 60 180 s
Liquidous temperature Time at liquidous	T _L	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T_p, T_C	220 235 °C **	245 260 °C **
Time (t _p) ** within 5 °C of the specified classification temperature (T _C)		20 s ***	30 s ***
Average ramp-down rate	T _p to T _{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

^{* =} Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and

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Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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PPD AB PD / PPD AB PM

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^{** =} For details please refer to JEDEC J-STD-020D.

^{*** =} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.



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